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This presentation contains forward-looking statements, including with respect to Intel's business plans and strategy, process and product roadmaps, and current and future technologies, as well as the anticipated benefits therefrom. Such statements involve many risks and uncertainties that could cause our actual results to differ materially from those expressed or implied, including: changes in demand for our products; changes in product mix; the complexity and fixed cost nature of our manufacturing operations; the high level of competition and rapid technological change in our industry; the significant upfront investments in R&D and our business, products, technologies, and manufacturing capabilities; vulnerability to new product development and manufacturing-related risks, including product defects or errata, particularly as we develop next generation products and implement next generation process technologies; risks associated with a highly complex global supply chain, including from disruptions, delays, trade tensions, or shortages; sales-related risks, including customer concentration and the use of distributors and other third parties; potential security vulnerabilities in our products; cybersecurity and privacy risks; investment and transaction risk; intellectual property risks and risks associated with litigation and regulatory proceedings; evolving regulatory and legal requirements across many jurisdictions; geopolitical and international trade conditions; our debt obligations; risks of large scale global operations; macroeconomic conditions; impacts of the COVID 19 or similar such pandemic; and other risks and uncertainties described in our earnings release dated July 27, 2023, our most recent Annual Report on Form 10-K and our other filings with the U.S. Securities and Exchange Commission. All information in this presentation reflects Intel management views as of the date hereof unless an earlier date is specified. Intel does not undertake, and expressly disclaims any duty, to update such statements, whether as a result of new information, new developments, or otherwise, except to the extent that disclosure may be required by law.

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# Pat Gelsinger

Intel CEO

It Starts Here


Bringing AI Everywhere
from client and edge
to the network and cloud


# ai.io

## Rich Felton-Thomas

Director of Sports Science
& Chief Operating Officer, ai.io

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# ai.io

## Intel technology powers every touchpoint across the ai.io experience

Xeon
**Cloud Computing**

Core i9
**Desktop, Laptop & Tablet**

PC Compute
**Connected Devices**

Networking
**End-to-end Optimization**

AI Models
**Optimized on OpenVINO**

Intel 3D AI
**Trained with Intel® Gaudi**

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# ai.io

Scan to learn more


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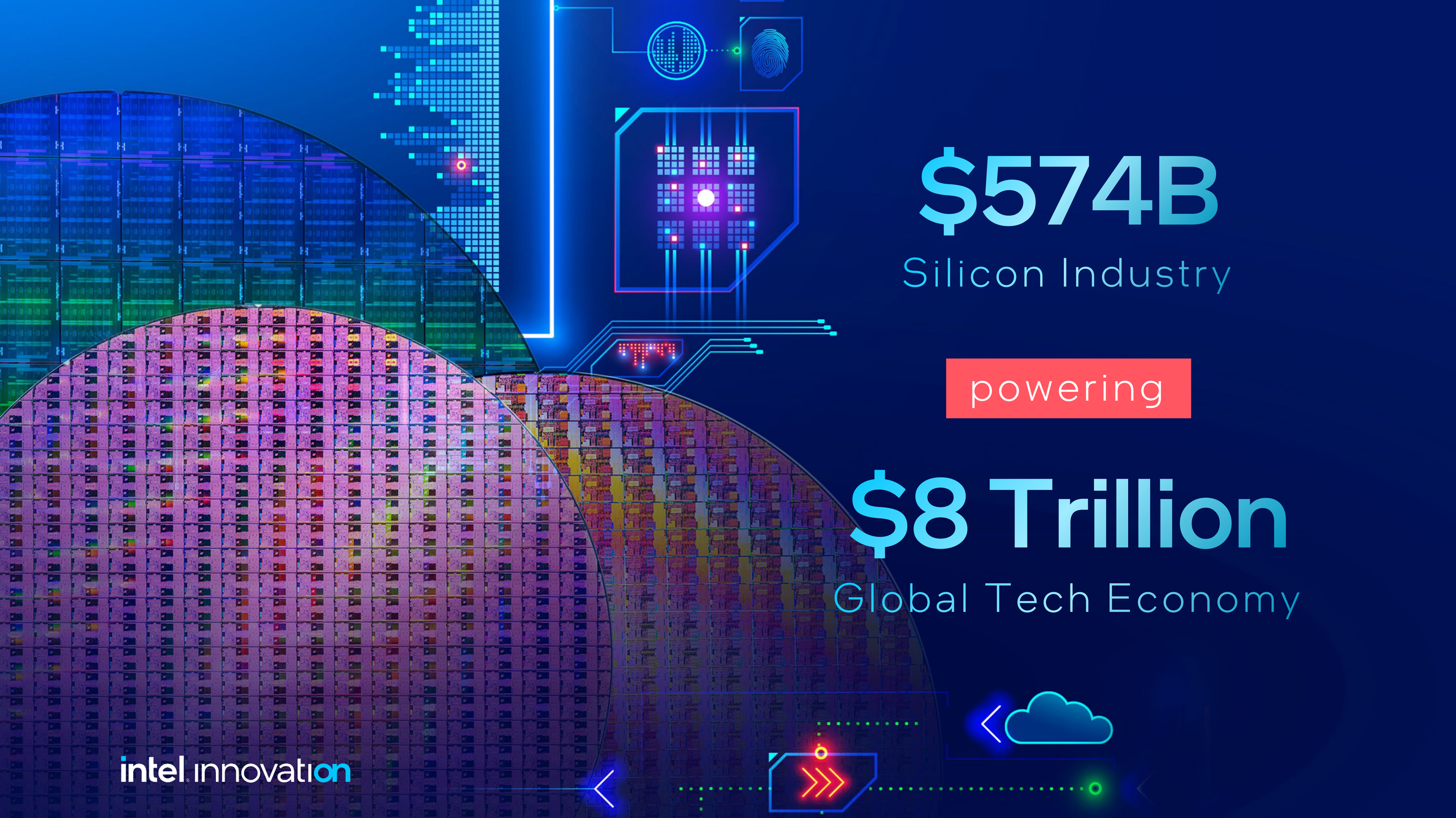
$574B
Silicon Industry
powering
$8 Trillion
Global Tech Economy
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# Siliconomy

*noun:*

An evolving economy enabled by the magic of silicon where semiconductors are essential to maintaining and enabling modern economies.

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# Siliconomy

**4x**

Increase
in connected devices
over past 5 years

**15x**

Growth
in connected devices
in 10 years

Base: 641 security decision-makers at consumer electronic device organizations
Source: A commissioned study conducted by Forrester Consulting on behalf of Intel, December 2022

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Developer Cloud
cloud.intel.com
access to our latest
accelerated hardware and software
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Build and test for best performance efficiency


Advanced AI capabilities toolkits

oneAPI

OpenVINO™

Multiplatform systems and clusters

intel Developer Cloud

cloud.intel.com

Available to all

intel innovation

cloud.intel.com

# Intel-optimized AI Software

supported by the latest CPUs, NPUs and GPUs

**1 week free**
of Intel Developer Cloud access
for every in-person attendee

it Starts Here

```
import webbrowser as siliconomy

accessibleDevelopment = “https://cloud.intel.com”
devNeeds = {"Open": True, "Choice": True,
            "Trust": True, "AI": True,
            "Client": True, "Edge": True,
            "Cloud": True }
if any(x is True for x in devNeeds.values()):
    siliconomy.open(accessibleDevelopment)
```

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# FUEL YOUR FIRE

Artificial Intelligence

Next Generation Systems

Platforms and Edge to Cloud

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DEEP RENDER

# Solving too much data and too little bandwidth problem

using Intel Developer Cloud

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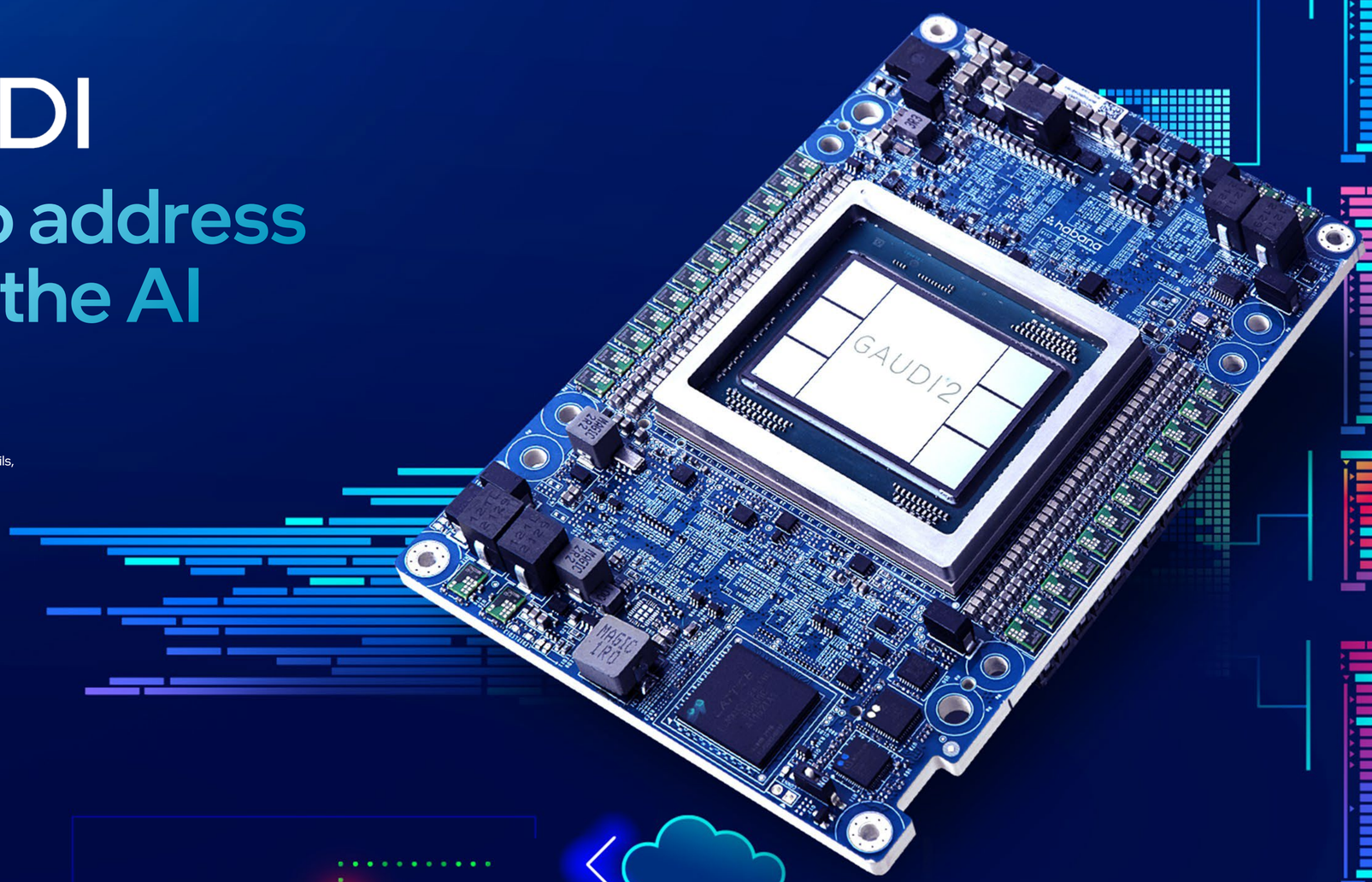
# intel GAUDI

## Commitment to address every phase of the AI continuum

Performance metrics based on MLPerf Inference v3.1 benchmark. For configuration details, see the results published by MLCommons. Results may vary.

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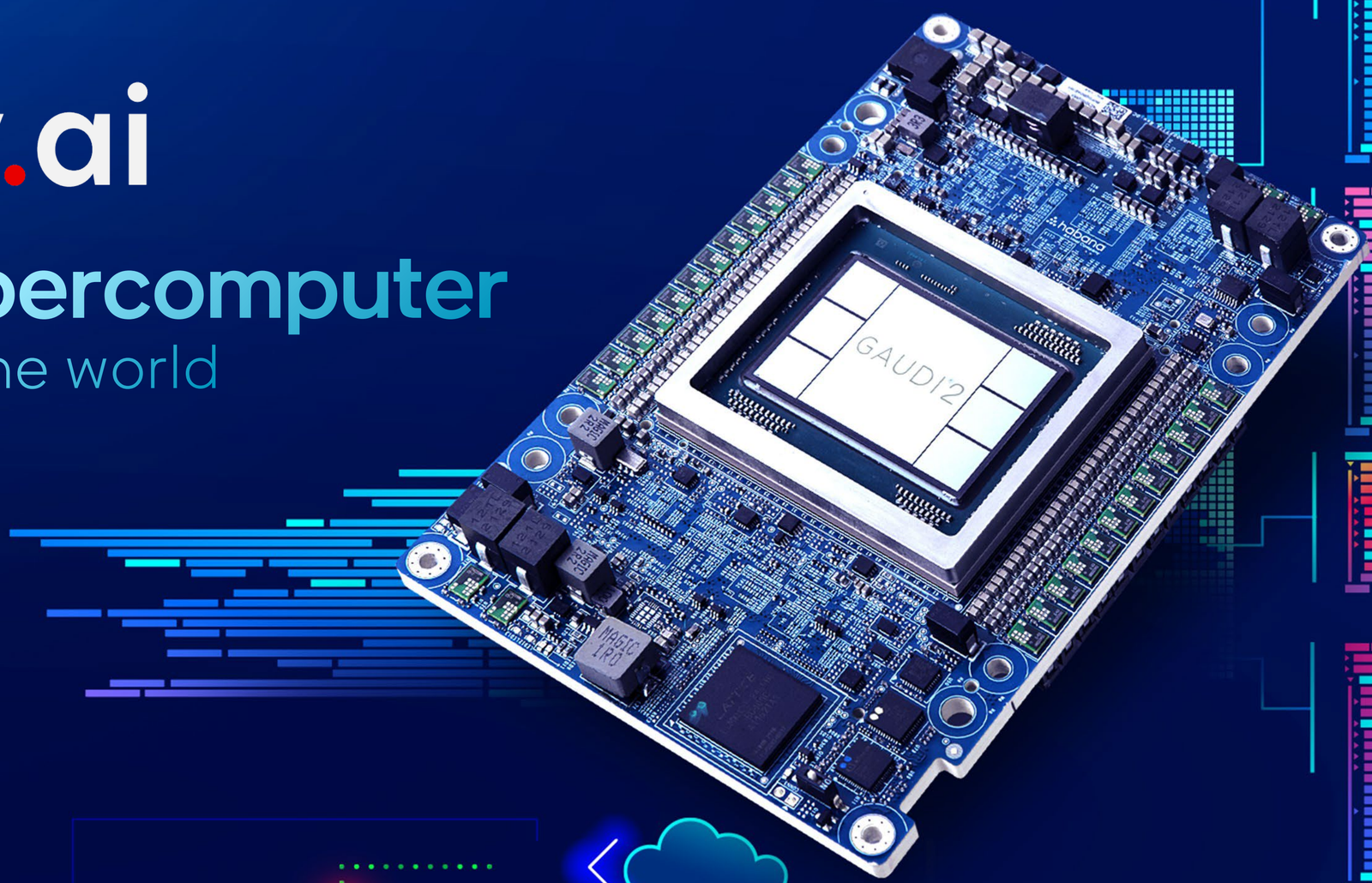
stability.ai

# Large AI Supercomputer

will be a top 15 in the world

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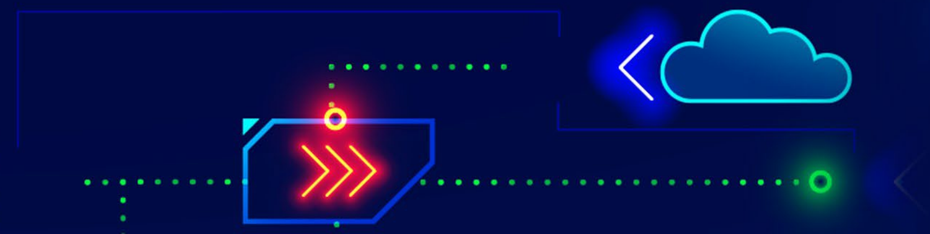
“Dell Technologies and Intel are collaborating to offer AI solutions to meet customers wherever they are on their AI journey. Combining the reliability of Dell PowerEdge servers – the industry’s top selling portfolio – with Intel technologies for general purpose and accelerated compute provides powerful systems for optimized AI. **PowerEdge systems with Xeon and Gaudi will support AI workloads ranging from large scale training to base level inferencing.** We look forward to helping customers transform their business with new applications with this powerful combination.”

**Jeff Clarke**
Vice Chairman and COO, Dell Technologies



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# 4th Gen Intel® Xeon® Scalable Processor

## MLPerf AI inference performance results: addressing every phase of the AI continuum

Performance metrics based on MLPerf Inference v3.1 benchmark. For configuration details, see the results published by MLCommons. Results may vary.


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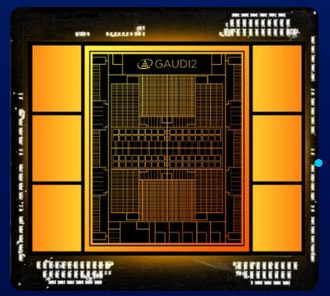
# AI Roadmap

## Bringing AI Everywhere

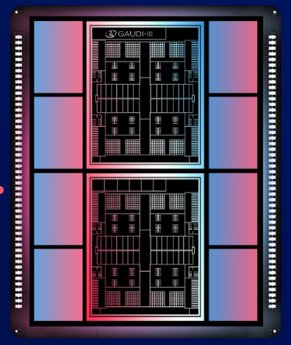



**4x** BF16

**2x** Compute

**1.5x** Network Bandwidth

**1.5x** HBM Capacity



**Intel® Gaudi® 2**
7nm

**Intel® Gaudi® 3**
5nm

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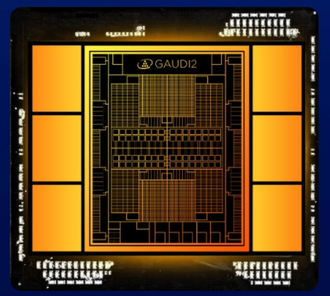
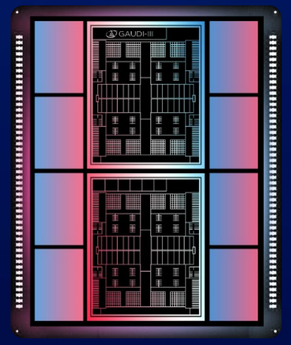
# AI Roadmap

## Bringing AI Everywhere




**Intel® Gaudi® 2**
7nm

**Intel® Gaudi® 3**
5nm

**Falcon Shores**
Codename

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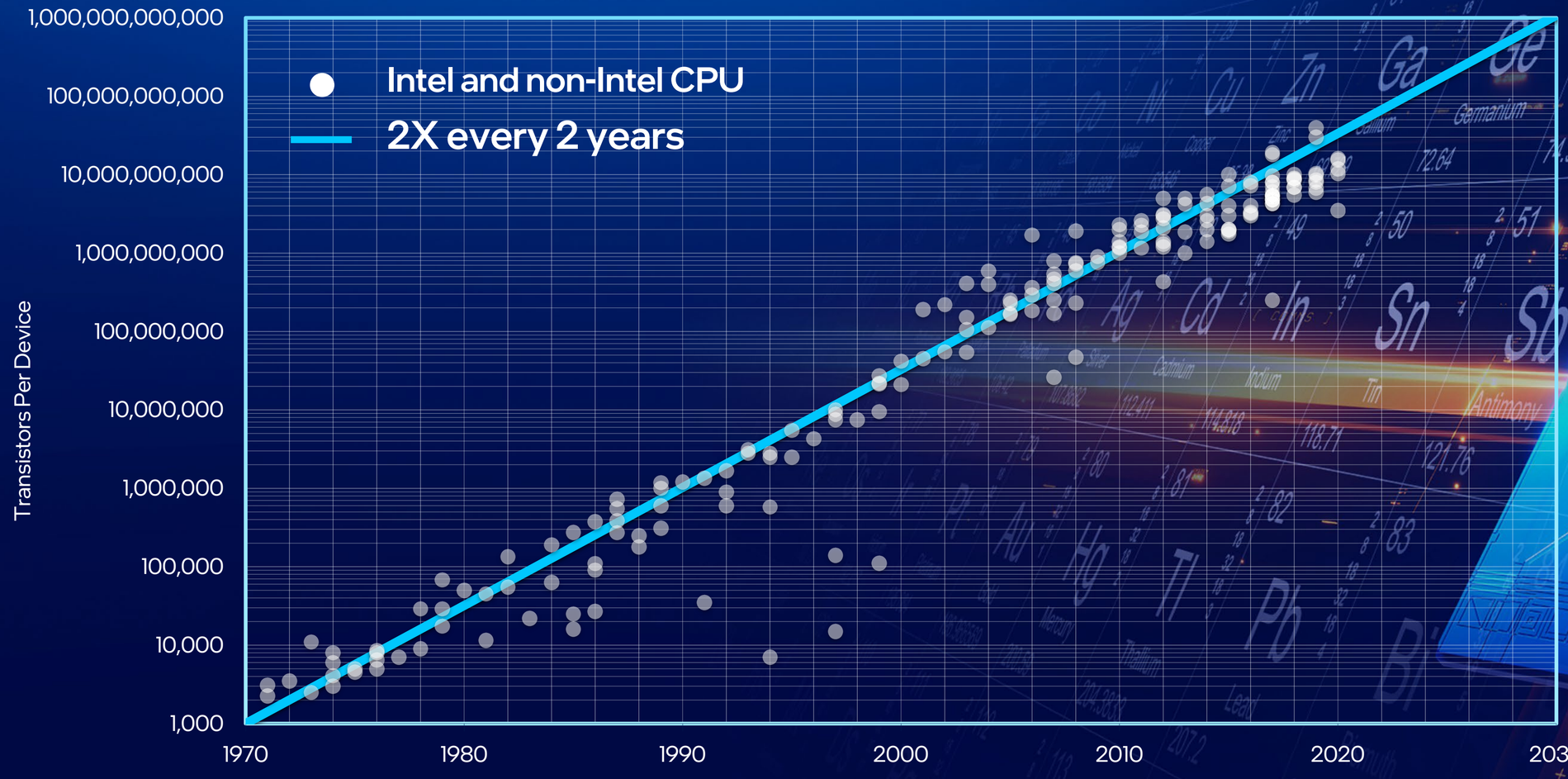
# Moore's Law is alive and well

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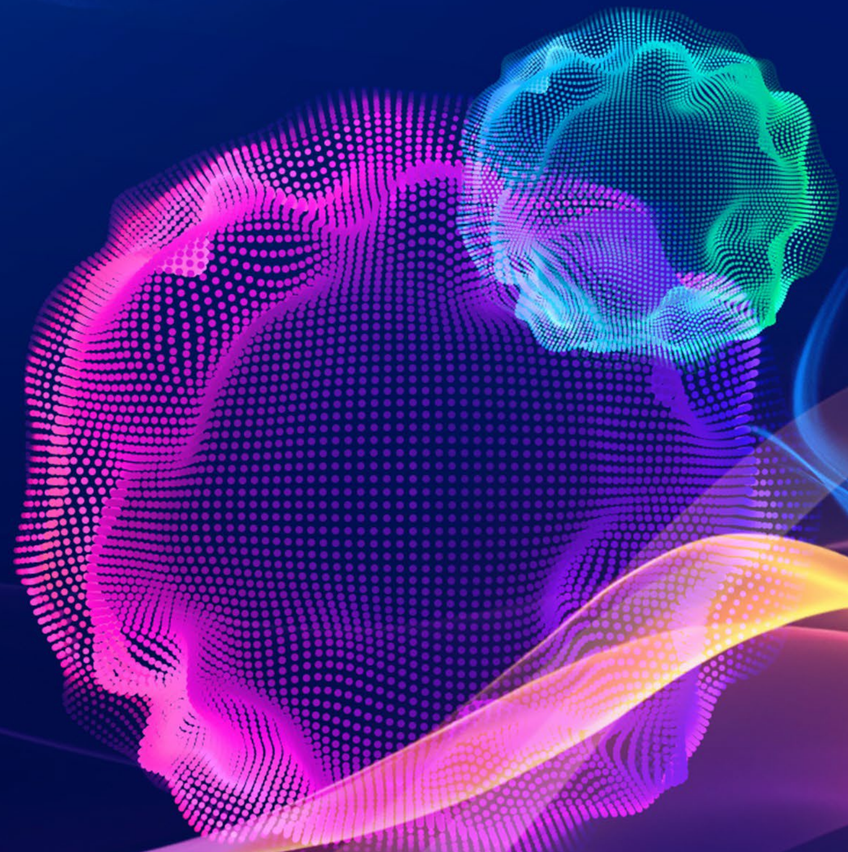
Scala Biodesign

# Using computational biology & gen AI

to dramatically speed up protein engineering process



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AI PC
Fundamentally transforming
the PC experience
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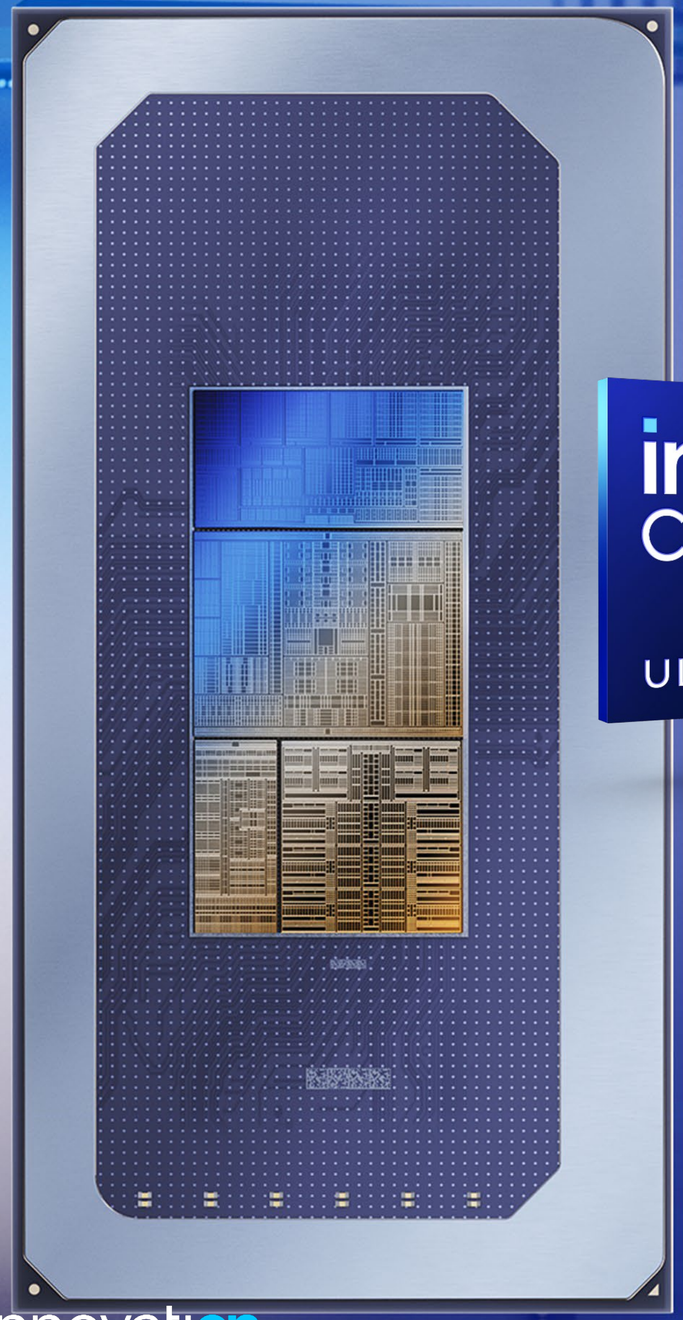

intel CORE ULTRA

# Age of the AI PC

## Launch of Intel® Core™ Ultra

December 14, 2023

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Microsoft

Copilot

+

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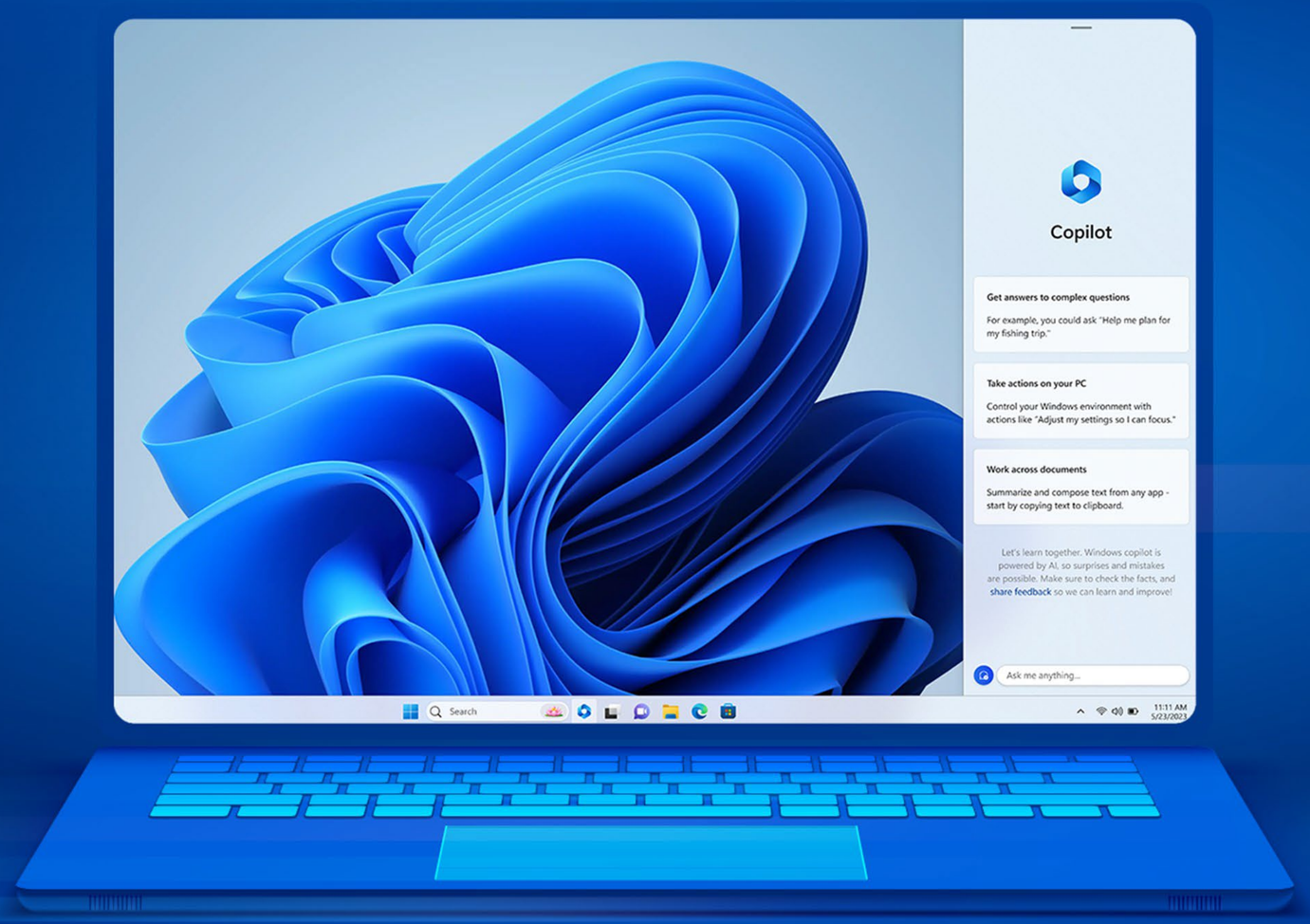


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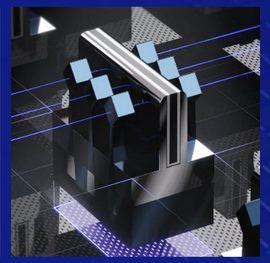
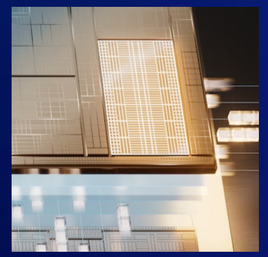
intel CORE ULTRA

First on **Intel 4**

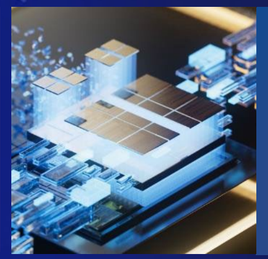


Using **EUV Technology**

Thunderbolt 4



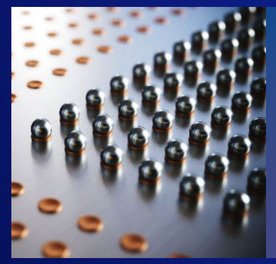
**3D Performance Hybrid** Architecture



Built-In **NPU AI Engine**

**Low Power Island** E-cores

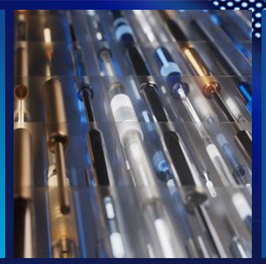
WiFi 7



**Foveros** 3D Packaging

New **P-core & E-core** Microarchitectures

Built - in intel ARC



Latest **Media & Display** Standards

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acer

# Jerry Kao

Chief Operating Officer, Acer

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The Upcoming
**Acer Swift**
Thin & Powerful Laptop

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The Upcoming
**Acer Swift**
Thin & Powerful Laptop

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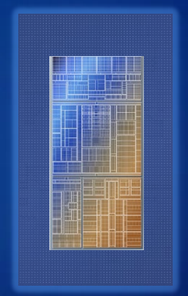
# Future Generations of Intel Core Processors

Realize your vision with AI PC capabilities



2024 — 2025

Arrow Lake

Lunar Lake

Panther Lake

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# Performance per unit of energy

must become our industry's mission

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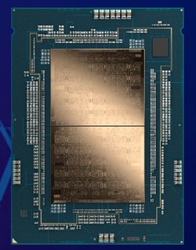
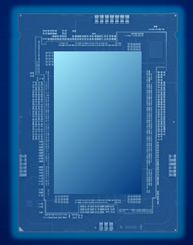
# Xeon Roadmap

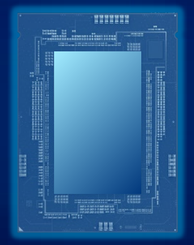
Designed to deliver higher performance using less energy measured as performance per watt



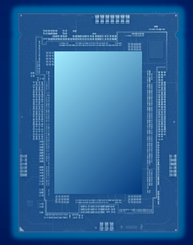
**5th Gen Intel® Xeon®**
Formerly codenamed
Emerald Rapids



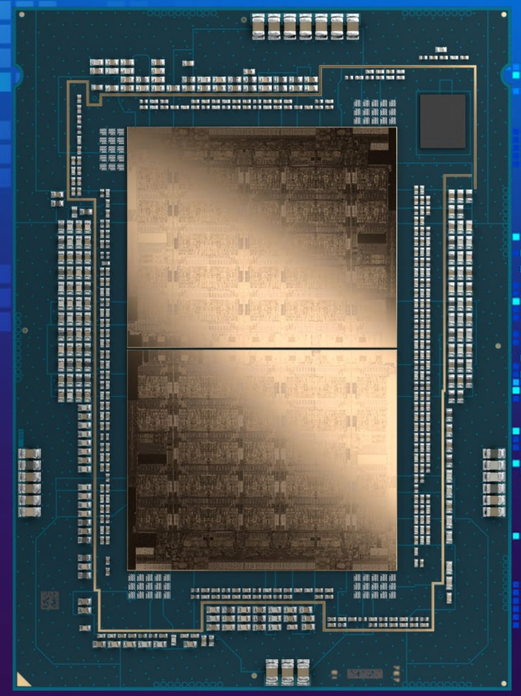
**Next Gen Intel® Xeon®**
Codenamed
Sierra Forest



**Next Gen Intel® Xeon®**
Codenamed
Granite Rapids



**Next Gen Intel® Xeon®**
Codenamed
Clearwater Forest



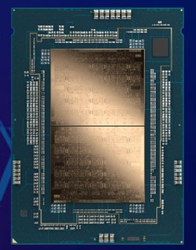
# Launch of 5th Gen Intel® Xeon® Scalable Processor

Formerly codenamed Emerald Rapids

December 14, 2023

# Xeon Roadmap

Designed to deliver higher performance using less energy measured as performance per watt



**5th Gen Intel® Xeon®**
Formerly codenamed
Emerald Rapids

**Next Gen Intel® Xeon®**
Codenamed
Sierra Forest

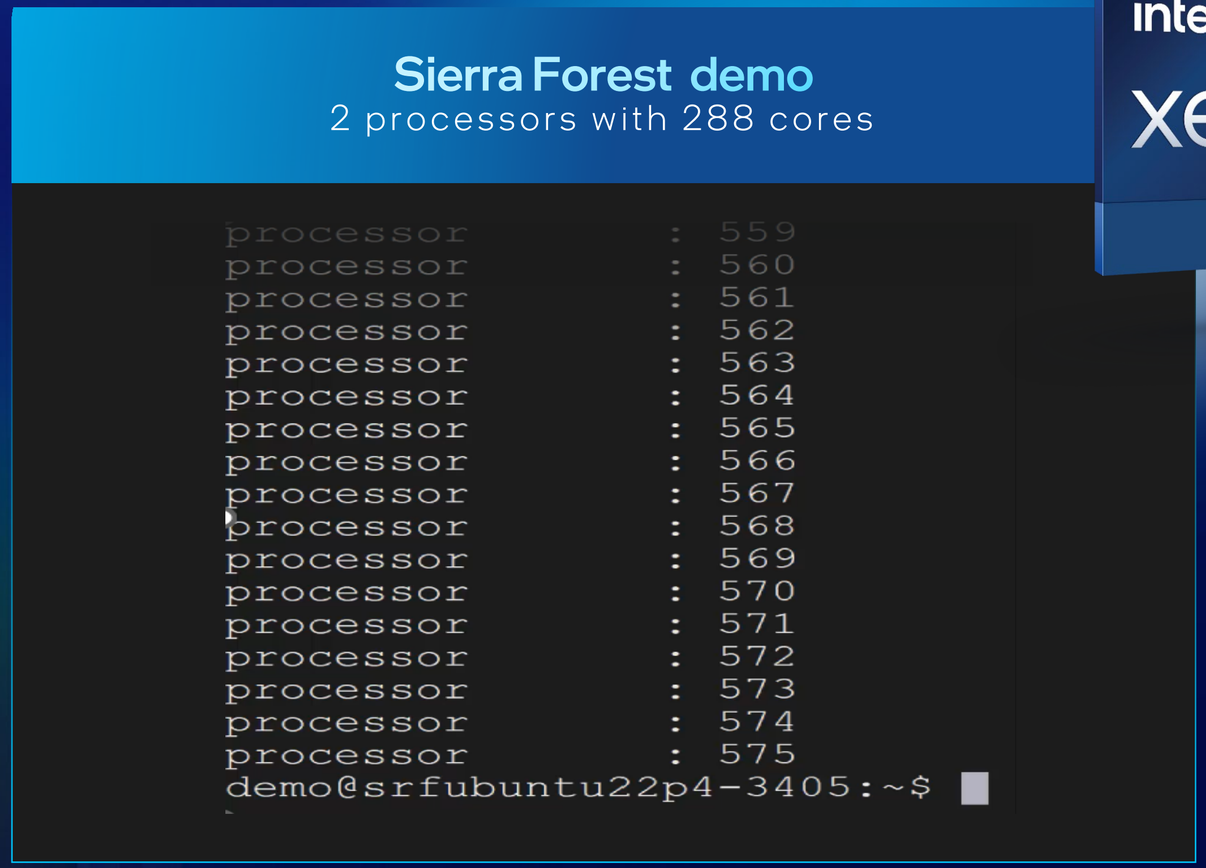
**Next Gen Intel® Xeon®**
Codenamed
Granite Rapids

**Next Gen Intel® Xeon®**
Codenamed
Clearwater Forest

Birch Stream Platform Architecture

Based on architectural projections as of August 21, 2023 relative to prior generation. Your results may vary.

## Sierra Forest demo
2 processors with 288 cores




intel XEON

# 288 cores
## Intel® Xeon® with E-core
Launch 2024

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# Security

Exponential compute creates
a vastly broader attack surface


intel
security
Security starts with intel
Learn more in Day 2 keynote

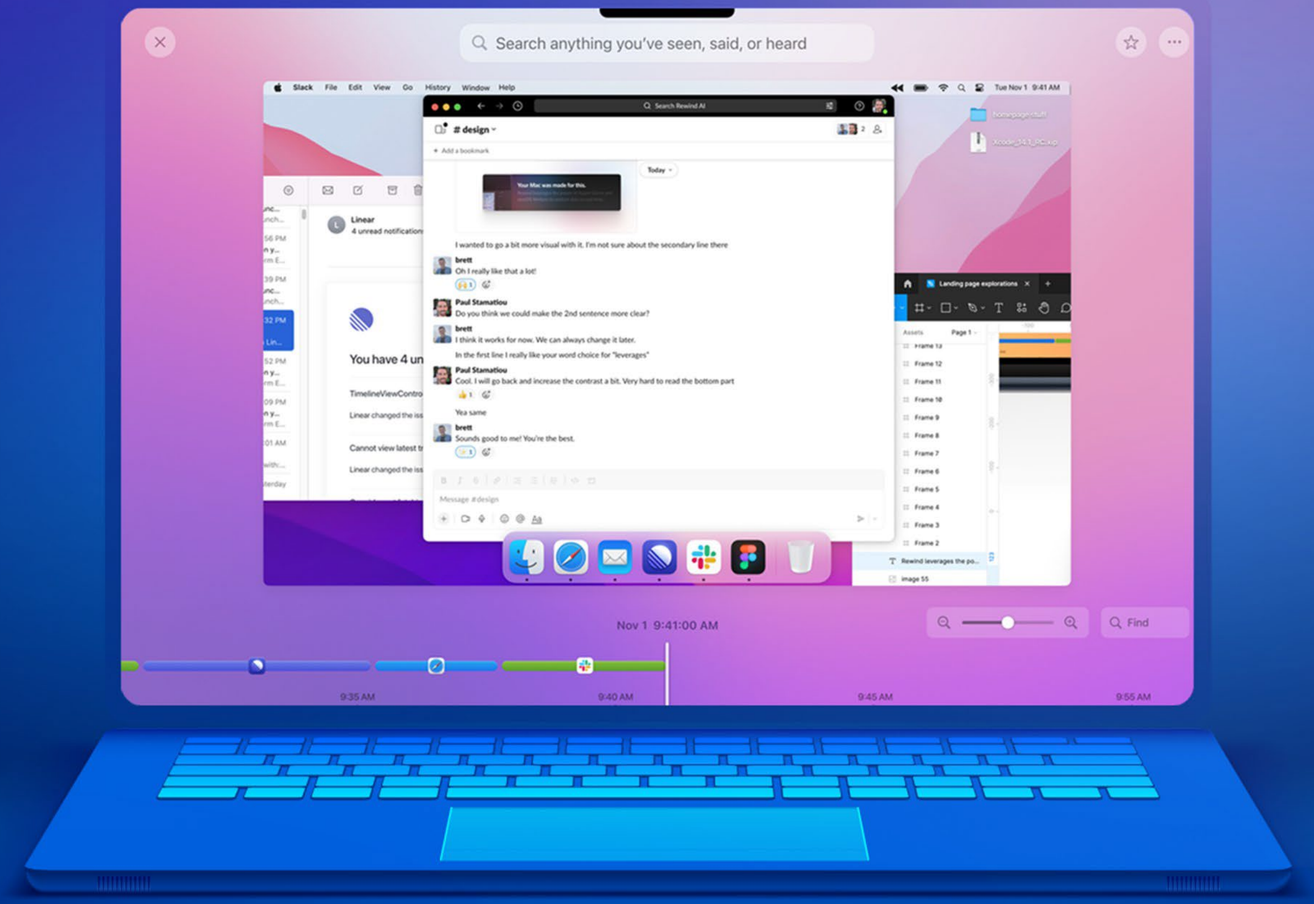

# Technology Superpowers

Enabled by Siliconomy

# Rewind

## Dan Siroker

Founder of Rewind.ai



# Rewind

+

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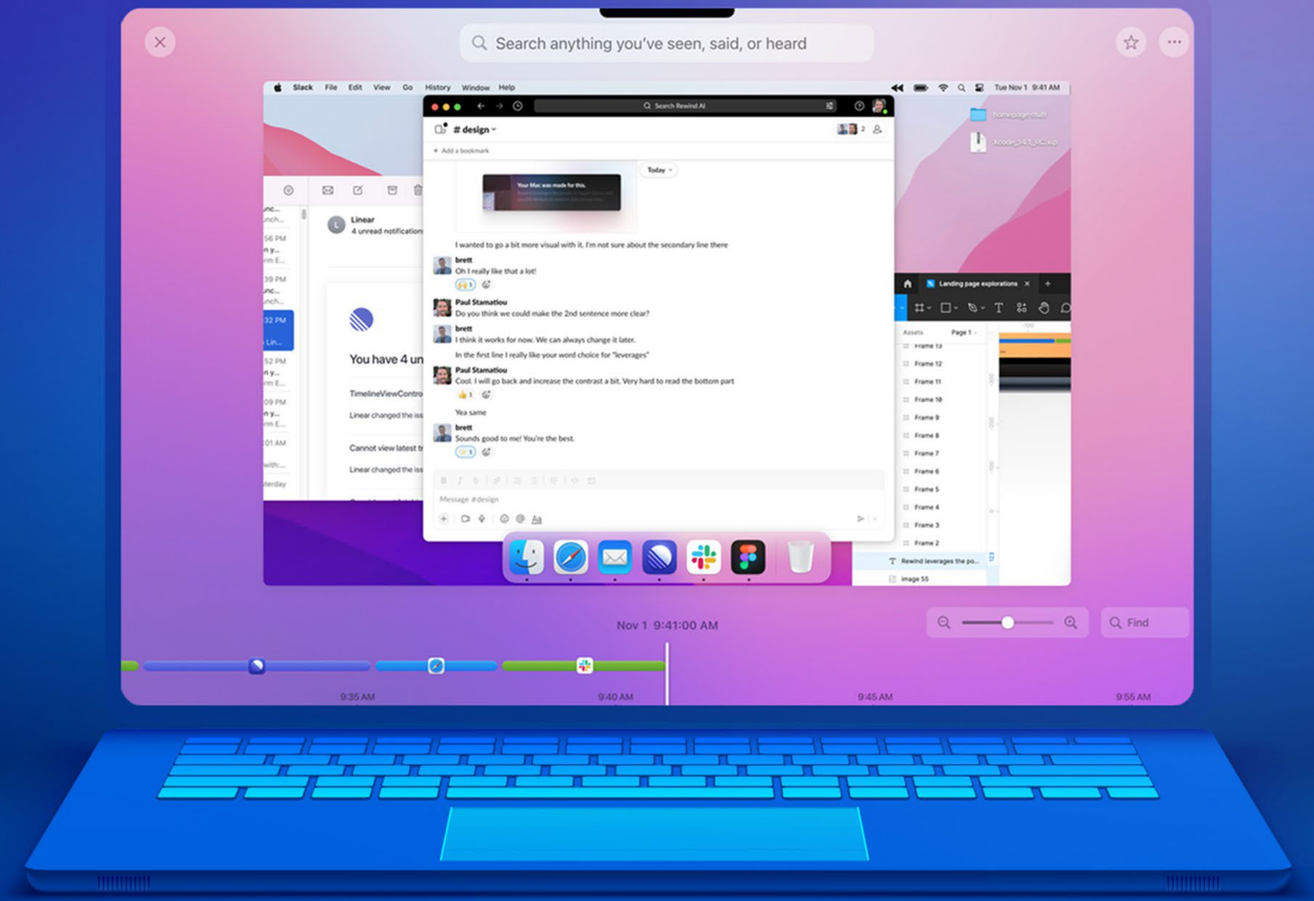


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Hearing Technologies
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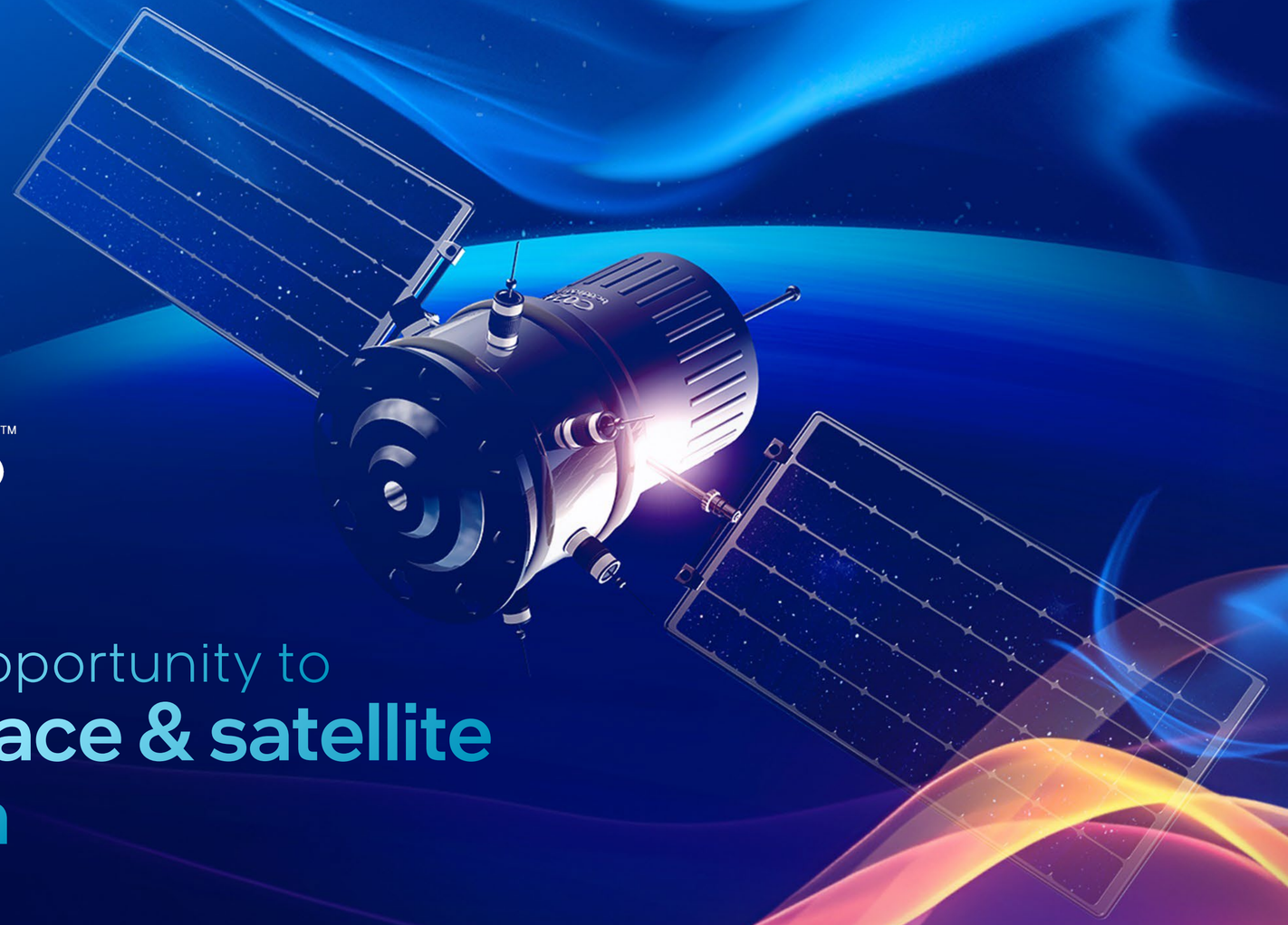
ANTARIS™
SOFTWARE FOR SPACE™



Identified massive opportunity to
**democratize space & satellite tech ecosystem**

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# OpenVINO™ 2023.1

## AI inferencing and deployment

Runtime of choice for developers
on client and edge platforms

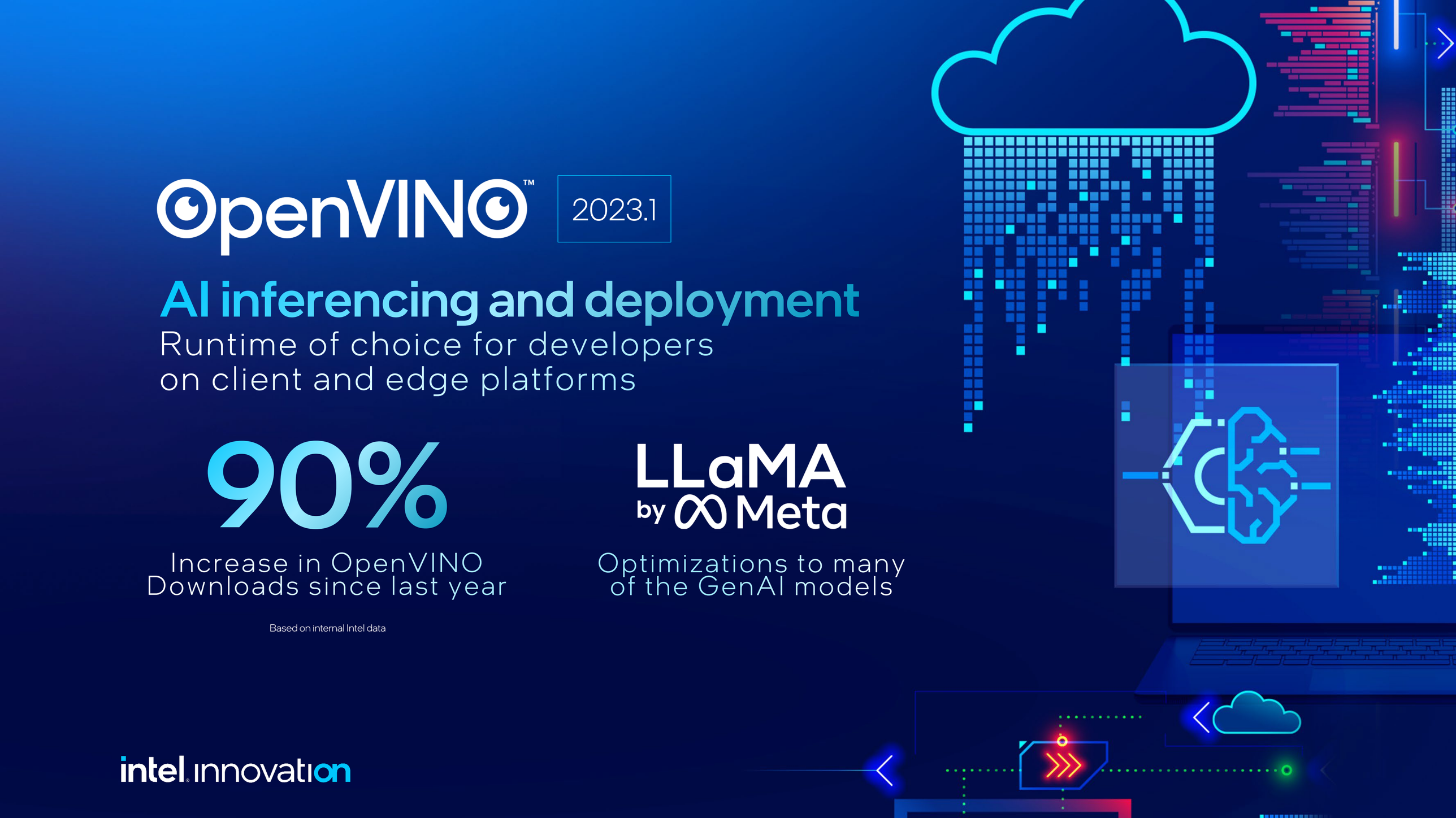
**90%**

Increase in OpenVINO
Downloads since last year

Based on internal Intel data

LLaMA by Meta

Optimizations to many
of the GenAI models



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# intel + arm

"Ensuring that developers have access to leading-edge tools and capabilities that allow them to write once and deploy anywhere is a key focus for Arm. We are pleased to see initiatives such as **OpenVINO which specifically target the deployment of complex AI models to Arm CPUs** and enable critical cross-platform support as more intelligent compute moves to the edge."

**Dermot O'Driscoll**
Vice President of Product Solutions,
Infrastructure Line of Business, Arm

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# Hybrid AI

Enhancing performance and accessibility

Bloom dolly LLaMA 2 Meta GPT-J Open-LLaMA LoRa

TensorFlow 飞桨 PyTorch Caffe ONNX mxnet Keras

OpenVINO™

Optimized Performance

CPU iGPU GPU NPU FPGA

intel CORE | intel ATOM | intel XEON | intel GAUDI | intel DATA CENTER GPU | intel iRIS Xe MAX GRAPHICS | intel ARC GRAPHICS | intel MOVIDIUS | intel FPGA AI Suite

Linux Windows macOS

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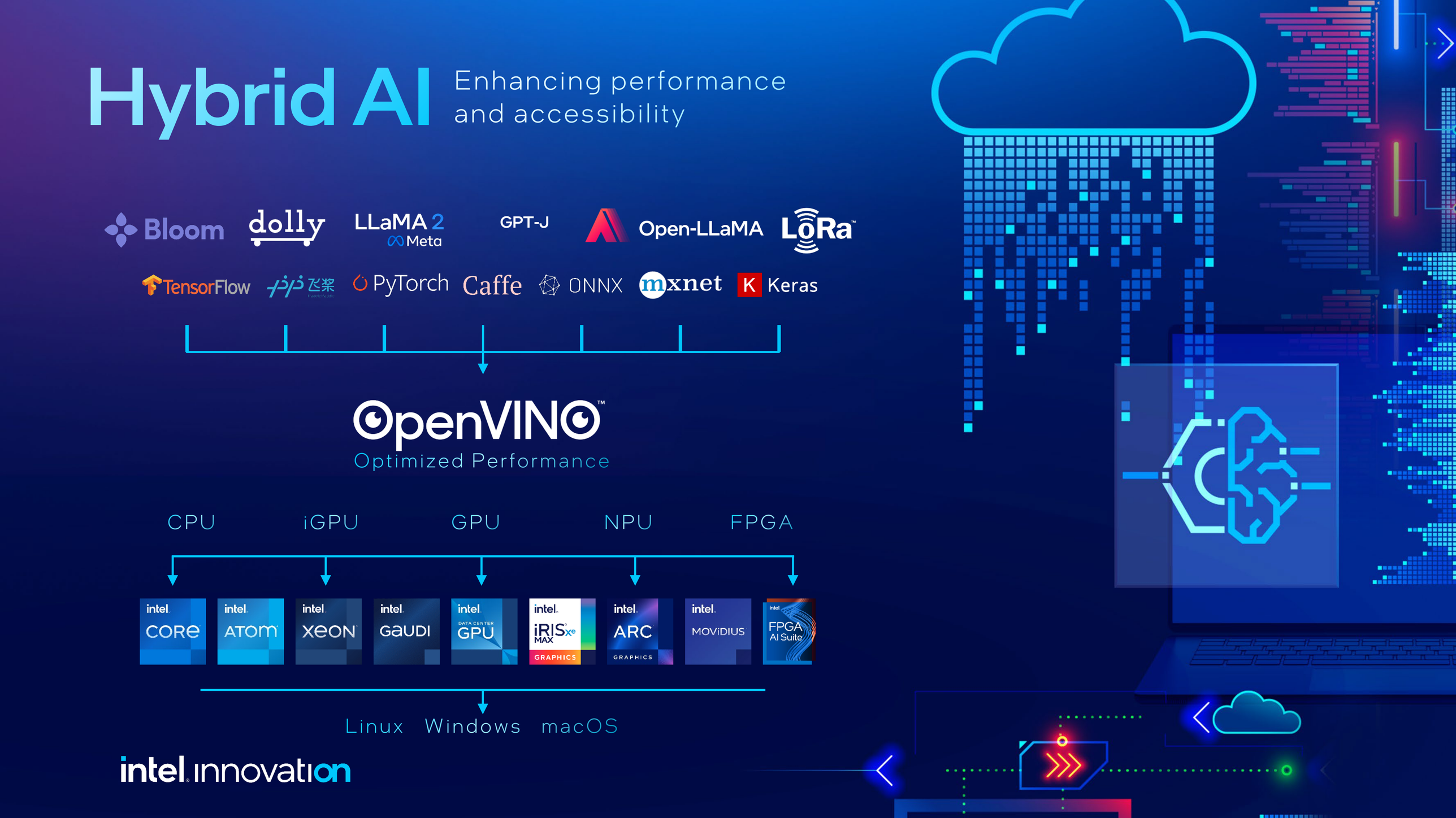

# Project Strata

the development of a modular edge-native software platform that will accelerate the adoption of AI-Automation at the Edge





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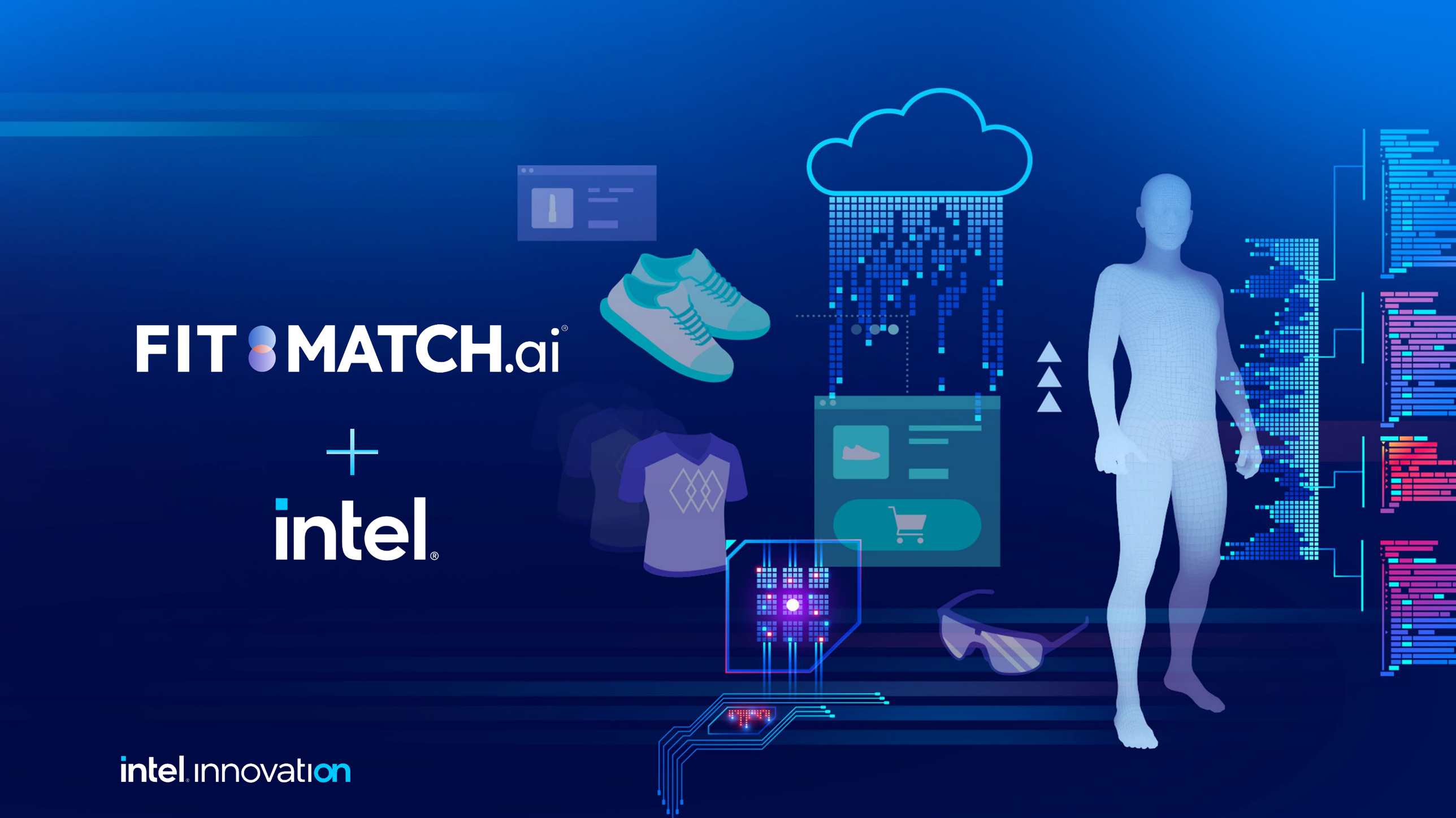
FIT MATCH.ai®

# Meera Bhatia

Chief Operating Officer,
Fabletics


FIT MATCH.ai
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DEEP RENDER

Intel 2023 Startup Innovator

# Award Winner

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# Beyond 5 Nodes in 4 Years

Looking into the future today


**NOW**
In high volume manufacturing

**NOW**
High-volume ramp

**2H 2023**
Manufacturing ready, sampling Sierra Forest and Granite Rapids

**2024**
On-track for manufacturing

**On Track**
Silicon goes into the fab in Q1 2024

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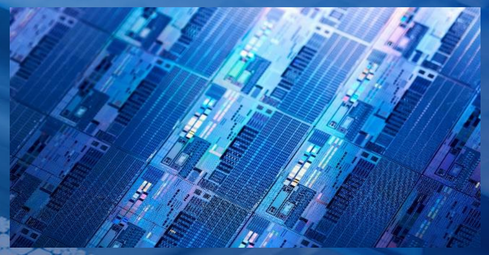
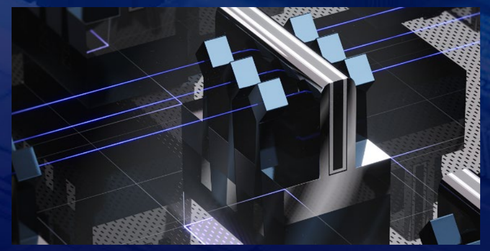
**Release the 0.9 PDK**
to foundry customers



**Test chips**
for multiple programs

ERICSSON arm

**Ribbon FET and PowerVia**
continuously evolving
process technology



**High-NA EUV technology**
development with 18A,
production with “Intel Next”

# Beyond 5 Nodes in 4 Years

## Looking into the future today

Intel 18A | Next | Next + | Next ++

**On Track**
Silicon goes into the fab in Q1 2024

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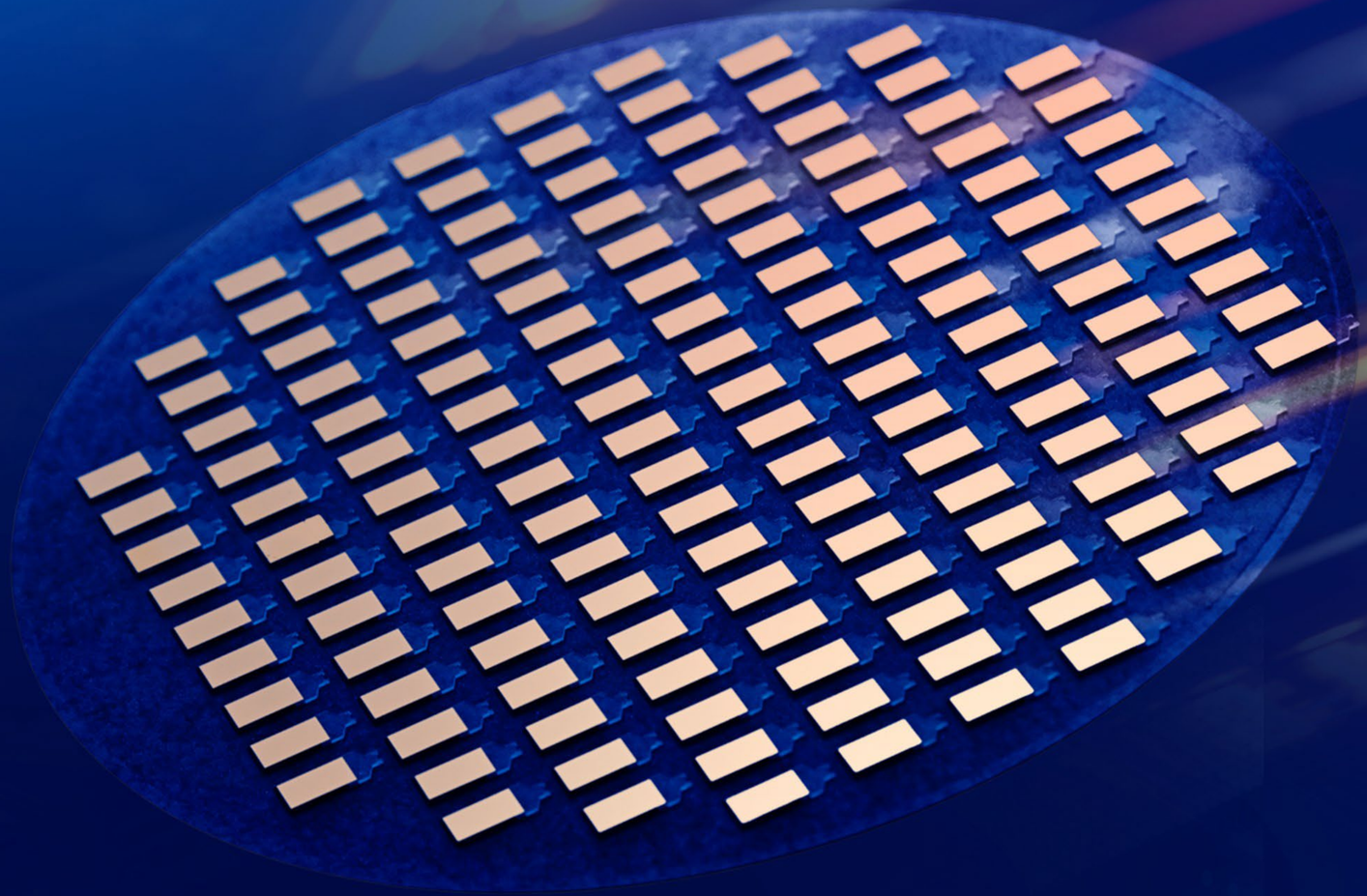
# Advanced Packaging

## Industry-first breakthroughs in glass substrates



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# $100 Billion

## U.S. Investments

Oregon

Arizona

New Mexico

Ohio

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# UCIe™ Universal Chiplet Interconnect Express

# 120+ Members... and growing!

## UCIe™ Board Members

Alibaba Group, AMD, arm, ASE GROUP, Google Cloud, intel, Meta, Microsoft, NVIDIA, Qualcomm, SAMSUNG, tsmc

## UCIe™ Contributor Members

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- Advantest
- AkroStar
- Alchip
- Alphawave Semi
- Amkor Technology
- Ampere
- Analog Devices
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- Astera Labs
- Ayar Labs
- Beijing Stream Computing
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- Broadcom
- Bosch
- Cadence
- Credo
- Ecarx
- Eliyan
- Ericsson
- eTopus
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- Global Unichip Corporation
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- JCET
- Juniper Networks
- Keysight
- Kiwimoore
- LG
- Lightmatter
- M2 Semi
- Macronix
- Marvell
- Mercedes-Benz
- Microchip
- Micron
- MediaTek
- National Institute of Advanced Industrial Science and Technology
- Neuron IP
- Physim
- Qualitas Semiconductor
- Point2Tech
- proteanTecs
- Rivos
- Semitronix
- Shanghai UniVista
- Siemens
- Silicon Precision Industries
- SK Hynix
- SkyeChip
- Socionext
- Synopsys
- Tenstorrent
- Teradyne
- Tongfu Microelectronics
- Truechip
- University of New Hampshire InterOperability Laboratory
- Xi'an UniIC Semiconductors
- UNISOC
- Veri Silicon
- Winbond
- Xpeedic
- Zero ASIC
- Zentel

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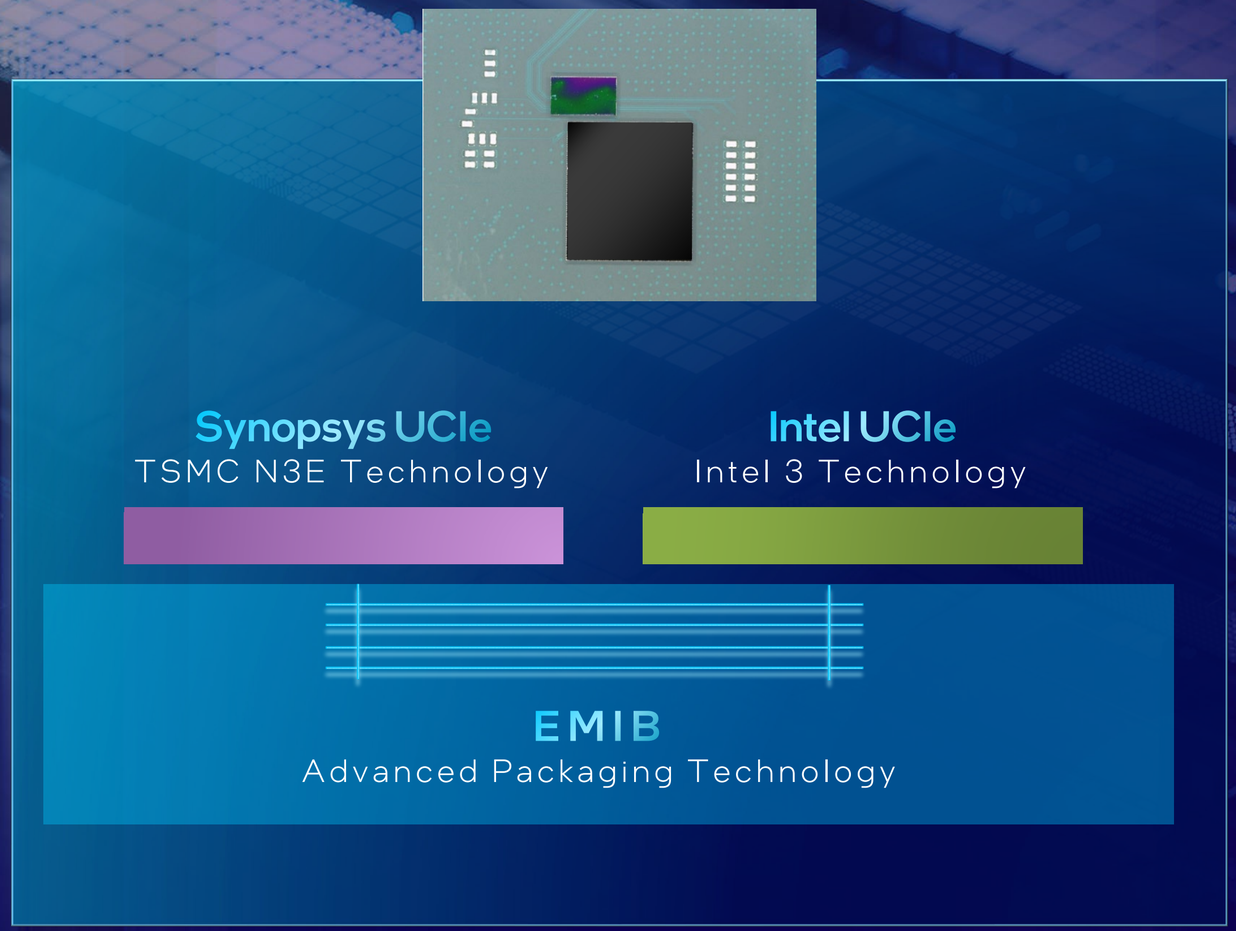


Advanced packaging technology codenamed

# Pike Creek

## Industry Interoperability Demo

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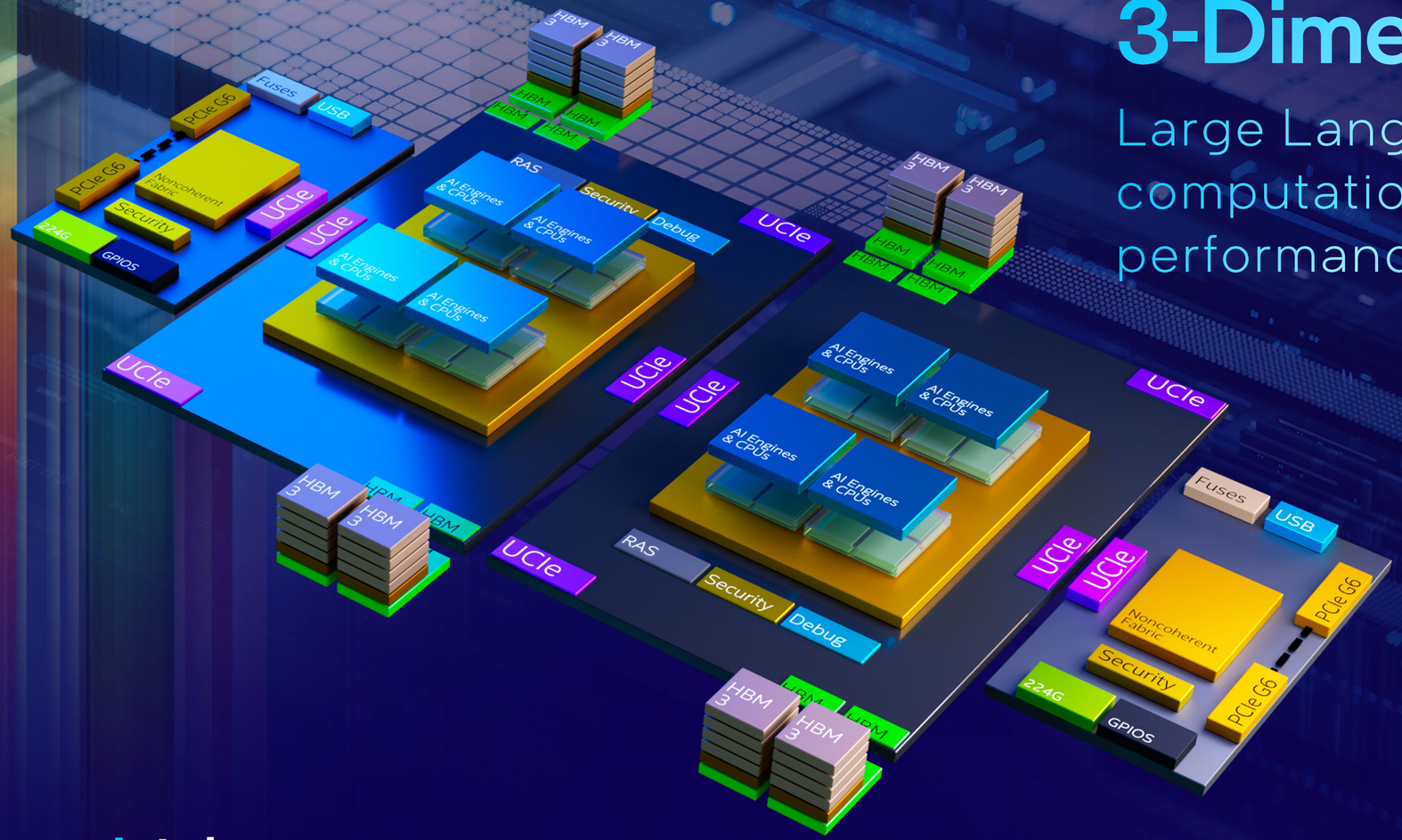
# 3-Dimensional Silicon

Large Language Model computation needs the highest performance silicon

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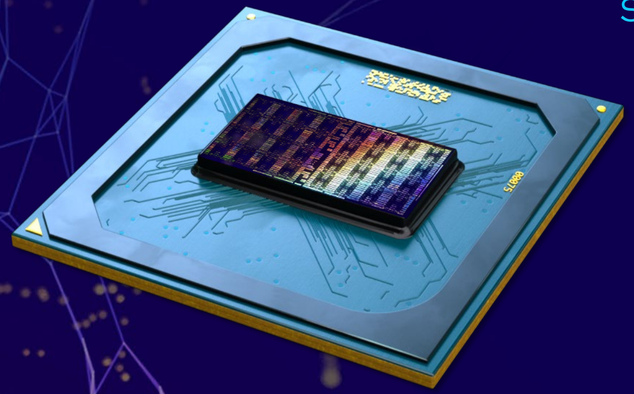
Leading efforts to advance

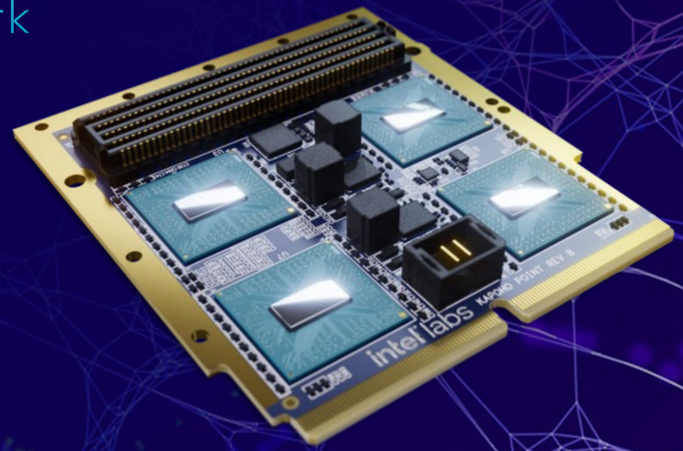
# Neuromorphic Computing

LAVA

**Open-source**
software framework



Codename **Loihi 2**
on Intel 4




Codename **Kapoho Point**
8-chip stackable

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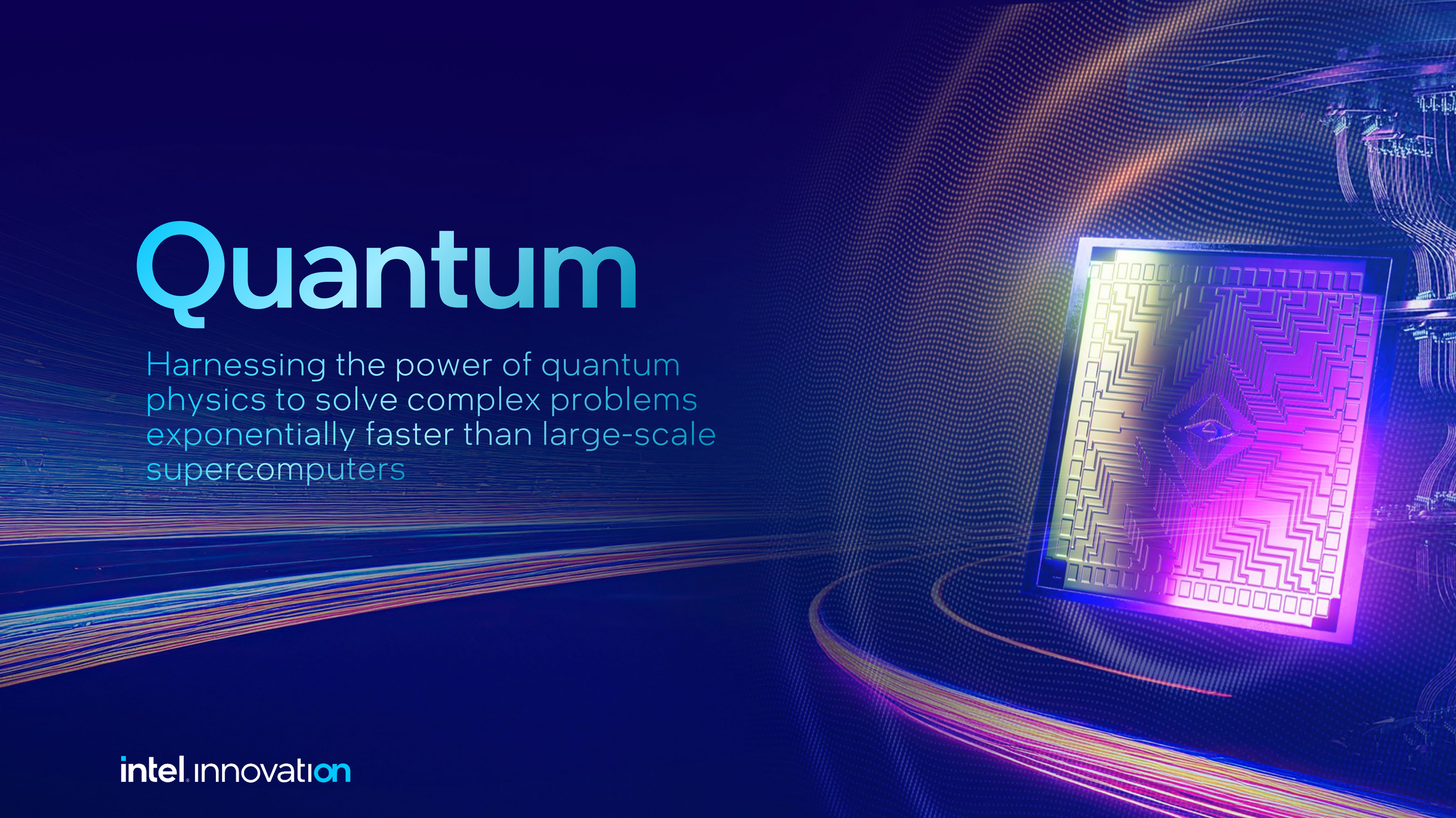
# Quantum

Harnessing the power of quantum physics to solve complex problems exponentially faster than large-scale supercomputers



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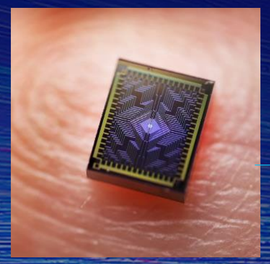
Intel quantum technology codenamed

# Tunnel Falls

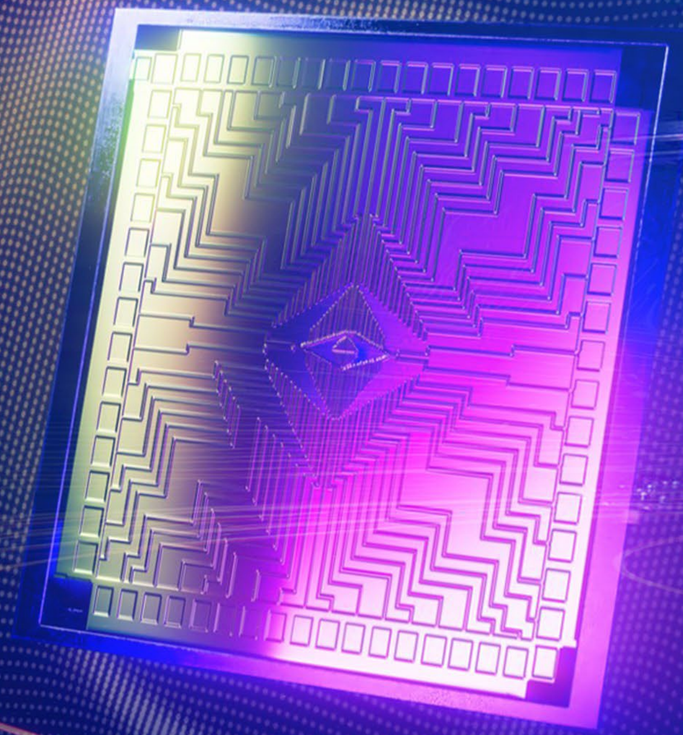


**12**
qubit device

**50x50**
nanometers

**~1 Million**
times smaller

**95% Yield Rate**
across the wafer



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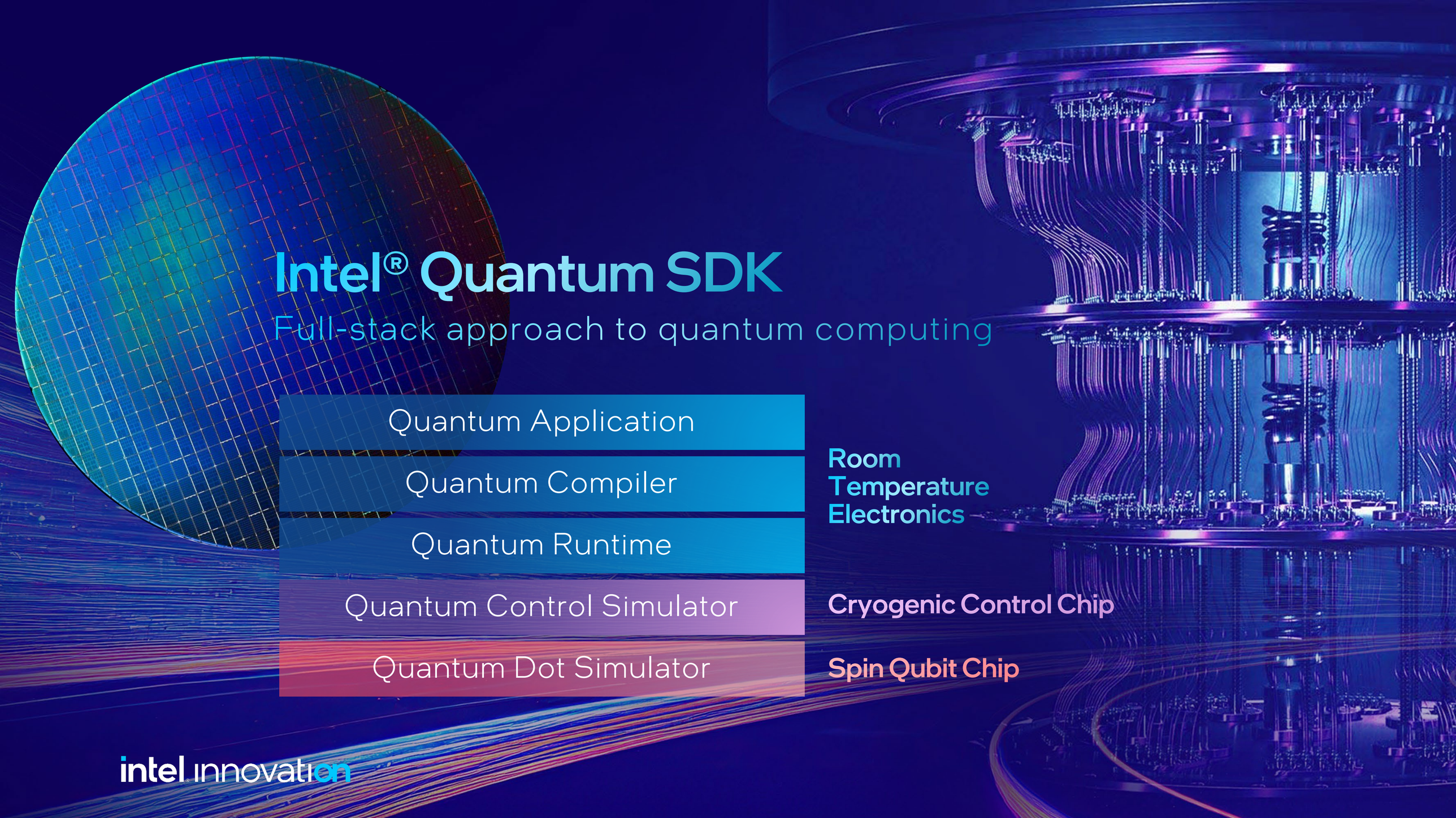
Technology performance varies by use, configuration and other factors. Learn more at www.Intel.com/ProcessInnovation


Intel® Quantum SDK
Full-stack approach to quantum computing
Quantum Application
Quantum Compiler
Quantum Runtime
Quantum Control Simulator
Quantum Dot Simulator
Room Temperature Electronics
Cryogenic Control Chip
Spin Qubit Chip
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# Lifetime Achievement Award

## Dr. Fei-Fei Li

Inaugural Sequoia Professor in the Computer Science Department at Stanford University, and Co-Director of Stanford's Human-Centered AI

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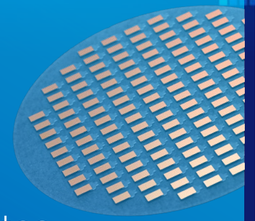
**Launch**
of Intel®
Core™ Ultra

**Launch**
of 5th Gen
Intel® Xeon®

## Advanced Packaging

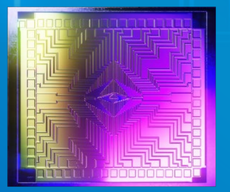
Industry-first
breakthroughs
in glass substrates



Software Initiative
codenamed
**Project Strata**

## Quantum

Innovating for the **future**



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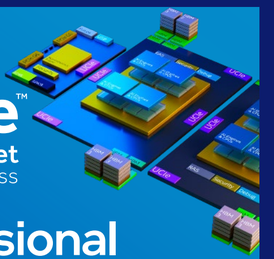

## 3-Dimensional Silicon

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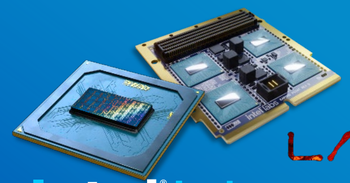

LAVA

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## Neuromorphic Computing

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