

Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE

Screen Name	Message Text
Steve	I'm back.
Leigh	Hi there
Leigh	Can we answer any questions?
MARUTHI SRINIVAS.RAMADURAI	Hello
Steve	Hi.
Steve	Which School are you from.
MARUTHI SRINIVAS.RAMADURAI	I am doing my Masters and will be graduating in December
Steve	very soon.
MARUTHI SRINIVAS.RAMADURAI	University of Minnesota,Twin Cities
Steve	ah, I've lived in Rochester, Mn before.
MARUTHI SRINIVAS.RAMADURAI	Oh thats awesome!
MARUTHI SRINIVAS.RAMADURAI	I recently had some interviews with Intel
MARUTHI SRINIVAS.RAMADURAI	and was invited onsite
Steve	which location.
MARUTHI SRINIVAS.RAMADURAI	but it was put on hold due to hiring freeze
MARUTHI SRINIVAS.RAMADURAI	Oregon, Santa Clara and Folsom
Leigh	=CONCATENATE("Intel is currently assessing our hiring plans for the remainder of the year. This effort is to ensure we carefully plan the remaining hires for the year. We are on track to have the best year in our company's history and as such, we want to ensure we stay on that track. We expect our planning cycle to take a couple of weeks. Once the planning is complete, we will be able to execute the remaining hiring plans.")
dilipkumar.gangapuri	Hi Leigh

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Leigh	You will still be considered for any positions he's applied to since that point
MARUTHI SRINIVAS.RAMADURAI	Ok.. thank you..
Leigh	Hi Dilipkumar
Abhilasha.Jain	Hi
Leigh	Hello Abhilasha - let us know what questions we can answer
Abhilasha.Jain	I wanted to know if there any available positions for Dec graduates in software field
Abhilasha.Jain	and I am an international student
Amanda	=CONCATENATE("We currently have active job openings posted here: http://www.intel.com/jobs/usa/students/ Not everything is posted yet though, as we are still working through our hiring plans for 2012. Keep an eye out over the next weeks and months as we get more jobs posted and begin the interview process!")
Amanda	More jobs area posting daily!
Leigh	=CONCATENATE("Intel will sponsor International Students if they meet the following requirements: They are pursuing a Masters degree in the following disciplines: Electrical Engineering, Computer Engineering or Computer Science (at this time, we cannot sponsor undergraduates). We are generally able to sponsor PhD's in all disciplines.")
Raji.Srikantan	Hi, I was wondering if the validation positions are suitable for new grads (with BS)?
Steve	Yes, we hire new grads in validation, it's a good place to start.
Steve	good area to be involved with many pieces of the processor puzzle.
Raji.Srikantan	The only hardware experience I have in from a digital design class, in which I learned Verilog. But I'm assuming these positions are a little more software based?

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Abhilasha.Jain	Are the inexperienced new grads have any entry level positions in software development?
Steve	having a BS is fine. (if that was the question also.)
Amanda	@ Raji-You may want to look at Pre-Silicon Validation positions
Raji.Srikantan	Ok. So, at this point, do I need to apply for other positions to be considered?
Steve	Intel has both Software and HW positions, even in Validation.
Amanda	@ Raji-they tend to be more SW focused
Vibudh.Mishra	Hi Steve
Steve	Hi Vibudh..
Abhilasha.Jain	because I only have experience of an internship in java
Rajiv.Villait	Hi, I'm Rajiv Villait....I am pursuing a Masters' in Chemical Engineering and will be graduating this December...I just wanted to ask if you prefer Electrical or Computer major for validation jobs or if you require it?
Vinay.Vijendra Kumar Lakshmi	Hi do we have a booth from PSV team?
venkata hari kishan.nallapati	hi steve
Vibudh.Mishra	I've experience with software validation. What are the opportunities for me if I want to apply as I will be graduating May'12.
Amanda	@ Raji-Applying directly to positions that closely match your interest is always a good idea
Rajiv.Villait	Also wanted to know if you would consider a Chemical Engineering Masters' student such as myself?
Zafar.Ahmed	Hi steve
Steve	experience is nice, (and something hiring managers definitely look at) but often times it doesn't matter what the experience is, Java proves you can program, and if the manager needs a programmer that works.

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Leigh	@Rajiv - we do hire chemE MS students, but for positions associated with our fabs, not in validation
Zafar.Ahmed	I have one question
Amanda	=CONCATENATE("@Vidubh-You may have heard recently that our CEO announced that we would hire twice the number of interns in 2012! We are really excited about this, and it creates unique opportunities for students set to graduate in late 2012 and beyond. Here is the press release with that announcement. http://bit.ly/oX68fL ")
Vibudh.Mishra	Is there any particular languages like Java, that you are looking for?
Steve	@zafar go ahead.
Rajiv.Villait	Thanks
Amanda	@Vidubh-Definitely apply for 2012 RCG positions now, but an internship may be a great opportunity in the meantime
justin.kellett	Hello, I am currently a junior Computer Engineering student at Penn state, I am pursuing a B.S and aim to eventually obtain a career in processor architecture. Is validation an appropriate field for me to gain experience in?
Zafar.Ahmed	are you mainly looking for ECE graduates or CS graduates for this position?
Steve	@Vibudh, in Validation we use C, C++, assembly, pearl, python, other simple scripting languages..
Zafar.Ahmed	I hhave 3 years of professional experience in C++ , Symbian C++
Vibudh.Mishra	@Amanda...Thanks a lot. I have applied for the job link provided with this chat. Should I have apply somewhere else to be considered for internships?
venkata hari kishan.nallapati	hi steve ae you hiring any recent graduates this year
Abhilasha.Jain	When can we expect the interview calls for this position?
Steve	Validation gives a wide breadth of exposure, since you have to know many pieces of the processor and system as a whole.
Steve	I probalby speak of Post-Si validation specifically.

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Zafar.Ahmed	I am planning to graduate at dec 2011
Zafar.Ahmed	ok
dilipkumar.gangapuri	Hello Leigh, im a grad student and will graduate in december, 2011
Amanda	@Vidudh-Always a good idea to apply directly to positions you're interested in. So if you see an internship opportunity you like-go for it.
Steve	since in Post-Si you're taking the procesor putting it in a ssystem, looking for failures and when things fail. the root cause can be Anywhere, the hunt begins.
Vikram.Jegannathan	Will knowledge of Flex and Bison be of any help for this position?
dilipkumar.gangapuri	i m looking for RCG positions
Steve	and that hunt for the root cause, may take you to various parts of the processor and/or system.
Leigh	=CONCATENATE("@abhilash - Response times to applications can vary. Some managers quickly review candidates and pick up the phone and call for a phone screen. Others can take more time to review their applicants. There's no standard interview timeline, but you should hear within a month or two if you'll be selected to interview")
Arohi.Desai	Hi Steve. I am pursuing my MS in EE from UT Arlington with a focus on embedded systems and wanted to know of any suitable positions. I have had good internship experience and will be graduating in May 2012.
justin.kellett	Thank you definitely a position for me to consider then
Steve	example: What if the problem is in a USB device..
Vibudh.Mishra	I have working experience with C, C++ and Python. I'm not that pro in assembly and perl. Will that make any difference in the selection criteria?
dilipkumar.gangapuri	Hello amanda
Amanda	Hi Dilipkumar

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Arohi.Desai	Hello Leigh. I am pursuing my MS in EE from UT Arlington with a focus on embedded systems and wanted to know of any suitable positions. I have had good internship experience and will be graduating in May 2012.
dilipkumar.gangapuri	im a grad student and will graduate in december, 2011 and looking for RCG positions
Amanda	@dilipkumar-you're at the right place. have you applied for positions with Intel?
Leigh	=CONCATENATE("@Arohi - We currently have active job openings posted here: http://www.intel.com/jobs/usa/students/ Not everything is posted yet though, as we are still working through our hiring plans for 2012. Keep an eye out over the next weeks and months as we get more jobs posted and begin the interview process!")
dilipkumar.gangapuri	yes i have applied few posiions but havent recieved any call yet
Amanda	=CONCATENATE("@Dilipkumar: Response times to applications can vary. Some managers quickly review candidates and pick up the phone and call for a phone screen. Others can take more time to review their applicants. The best thing you can do is apply to all jobs that you feel interested in and at least 80% qualified for! We have many recruiters that review resumes and this is a good way to be seen by all.")
dilipkumar.gangapuri	what specific area does your team work for validation or design
Vishal.Guntur	=CONCATENATE("Hi steve! I'm a MS EE student and I will be graduating in Dec 2011. I'm looking for RCG positions. I have a solid background in VLSI fundamentals, digital design, computer architecture, Verilog and C++... I also have around 2 yrs or work experience in shell and perl scripting (which i'm sure helps :)). are you looking for any other specific skills for you team ?")

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Arohi.Desai	@leigh: Thanks a lot and I have been applying to suitable positions for internship as well and dont seem to be hearing back. Can you let m eknow some key points managers are looking for>?
Steve	One thing I want to generically point out (pehraps this is for those who still have another year or so to go.) but the more Intershps you have, the better your resumes look..
Scott	Hi Mishra - C, C++ and python are more important to what our group does.
Zafar.Ahmed	ok
Venkatswamy.Gandham	Hello steve, I have applied for several positions at Intel but couldn't get any calls. I want to know where I am lacking in skills.
Leigh	@Arohi - managers look for good internship experience, as Steve just mentioned. Also be sure to call out any academic projects that are relevant to the jobs you are applying for
Leigh	@Arohi - good communication skills and leadership experience are also important
Scott	Dilipkumar - we are mainly focused on post si validation but we do have some interaction with design.
Tom.Sommerville	Steve, do Managers consider undergrad research along similar lines to an internship?
Steve	Vishal, hi, yes, those are applicable qualifications... and any work expeience would be good too.
Vibudh.Mishra	Thank you so much Scott. I have already applied to the RCG job link and would like to know about the more specific technical skills you are looking for.
Huan.Lin	Hello Steve. Im a MS. EE major in MSU now will graduate in Dec. 2011. I had my B.S in EE and a B.A in Supply Chain Management. Will this give me a plus for the consideration?
dilipkumar.gangapuri	I am proficien tin c, c++ , Verilog, have basic understanding of perl and system verilog.
dilipkumar.gangapuri	do i need to have any other knowledge

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Vinay.Vijendra Kumar Lakshmi	Hi, I have cleared the Phone screen of PSV team, Folsom? Whom should I address my questions?
Leigh	@Tom - I know your question was directed to STEve, but I'll answer. Yes, we think undergrad research is a great experience to have on your resume! If you have the chance to do both that and some industrial experience you will be in very good shape
Steve	Venkatswamy, what are your work experiences?
Amanda	@Vinay-were you working with a recruiter?
Venkatswamy.Gandham	I have worked as a technical associate at wipro
Vishal.Guntur	@steve.. thats great! so what kind of issues does your team regularly deal with in validation?
Vinay.Vijendra Kumar Lakshmi	@ amanda: Nope ! A technical lead
Steve	Huan: the BA isn't applicable to Validation work, but the BS is, I'm an EE with a BS myself. though Years ago.. :-)
Huan.Lin	cool
Amanda	@Vinay-the tech lead would be a great follow-up contact then
dilipkumar.gangapuri	can you take a look at my resume and consider for the any job in your team that suits my profile
Vinay.Vijendra Kumar Lakshmi	@Amanda: He has forwarded my resume to the HR, They are responsible to schedule my onsite.
Tom.Sommerville	=CONCATENATE("@Leigh - Thanks. Unfortunately I wasn't able to secure an internship, I was working FT for much of my time in college, but I have a year of research experience. Are hardware Validation positions open to Computer Engineers, or are they primarily suited for Electrical Engineers?")
Huan.Lin	I 'm currently doing research on parallel computing of GPU, is there any position that I could utilize my skill in Intel
Steve	Vishal: We're professional problem solvers of the processor Kind.

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Harika.Suram	Hello Amanda, I graduated in May 2011.I applied to lot of positions of my Interest. Some times I got a response asking me to fill pre-screen questionnaire and then after I did not get any reply. Is there anything that I need to do?
Steve	Vishal: Some data is corrupted. (Cache, Memory, etc.) we're on the job.
Vinay.Vijendra Kumar Lakshmi	@Amanda: He is no more responsible for my application. So I guess he does not reply to my mails any more
Venkatswamy.Gandham	I don't have any prior experience in VLSI design or validation.Except for in my masters
Leigh	@Tom - we'll hire both CE and EE for these types of jobs
Amanda	@Vinay-I'm sorry we don't have specific information on this chat for you
Vishal.Guntur	@steve .. so its system level validation then.. also overshoots, undershoots that are always there to cause problems right!
Vinay.Vijendra Kumar Lakshmi	@Amanda: Whom should I be contacting? That is the only question I have
Amanda	=CONCATENATE("@Harika-Response times to applications can vary. Some managers quickly review candidates and pick up the phone and call for a phone screen. Others can take more time to review their applicants. The best thing you can do is apply to all jobs that you feel interested in and at least 80% qualified for! We have many recruiters that review resumes and this is a good way to be seen by all.")
Karthik.Ananthanarayanan	=CONCATENATE("@Steve: I have interened at Intel Santa Clara with the DFT team, i am into electrical engineering with emaphasis on COver Computer Architecture, VLSI design and some software like operating systems. I am open for soop positions for spring 2012 and also full tiem positions from MAY 2012.")
Steve	Venket: that sounds good. If you could have someone review your resume, that would probably be beneficial.

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Vibudh.Mishra	@Scott....Thank you so much Scott. I have already applied to the RCG job link and would like to know about the more specific technical skills you are looking for.
Steve	(review for content, word usage, way to say it with more gusto perhaps, you get the idea..)
Tom.Sommerville	For those of us who applied to #609815, will those applications be reviewed by all managers and recruiters here today?
Venkatswamy.Gandham	yes
Venkatswamy.Gandham	there are some flaws in my resume
Dharmesh.Chheda	Hi Steve, I was interviewed for Intel in august 2011 for Firmware Developer, gave an onsite as well , then I got the reply that position is freezed. So what are the further steps?
Steve	Vishal: by overshoots, undershoots, do you mean Analog issues? Yes, we're involved with analog issues when necessary.
Amanda	@Vinay-I'm sorry I don't have specifics for you. I'm sure the Tech Lead will follow up with you when he has more information
Karthik.Venkataraman	Hi Do you consider C++ dev for this position
Vibudh.Mishra	@Scott...Thank you so much Scott. I have already applied to the RCG job link and would like to know about the more specific technical skills you are looking for.
Karthik.Venkataraman	Iam karthik from UFL doing MS in Computer Engineering
Dharmesh.Chheda	And also, I would like to know the basic requirements that the managers see for this positions

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Amanda	=CONCATENATE("@Dharmesh-Intel is currently assessing our hiring plans for the remainder of the year. This effort is to ensure we carefully plan the remaining hires for the year. We are on track to have the best year in our company's history and as such, we want to ensure we stay on that track. We expect our planning cycle to take a couple of weeks. Once the planning is complete, we will be able to execute the remaining hiring plans.")
Leigh	@Tom - yes, we will keep the resumes of all participants in the chat for the recruiters to review. I'd also recommend you apply directly against any other positions that interest you at www.intel.com
dilipkumar.gangapuri	@ Leigh can you take a look at my resume.
Karthik.Venkataraman	I am looking for Full time
Amanda	If you do see something posted, then that means we have been approved to fill that opening. Please apply to all positions you have interest in!
Leigh	@Dilipkumar - I'm sorry but we will not be able to review individual resumes today
Dharmesh.Chheda	@ Amanda Do I have to give the interviews all over again?
Vinay.Vijendra Kumar Lakshmi	@Amanda: Thanks for the info. Can you please let me know if PSV team, Folsom has a booth today?
Amanda	@Vinay-I don't believe so.
Tom.Sommerville	General Question: What do you like about the Validation positions in your group?
Harika.Suram	@Amanda-Could you please look at my resume if possible.
Amanda	@Dharmesh-I would believe your original interview would still be valid
dilipkumar.gangapuri	can all the hiring managers will be able to see the resume for the job which i applied (Job Number: 609815)

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Dennis.Chapman	Hi Leigh, I am a Engineering major with an interest in software an debugging but very little background, is there an entry level positions for students who are critical thinkers but don't neccessarily have a background in SW Engineering
Amanda	@harika-I'm sorry but we will not be able to review individual resumes today
Dharmesh.Chheda	Can you also tell me the basic requirements that the managers look for inthese kind of positions?
Scott	Hi Tom - I really like the visibility and variation of the projects we get to work on - especially since most of our projects are cutting edge.
Vishal.Guntur	=CONCATENATE("@steve.. yes.. i mean let's say that the DVID requirement is 0.6 V and some noise/issue makes it impossible for the VRM to supply regulated output within the specified time or the voltage overshoots above the specfied max level.. it could lead to some serious damage right?")
Steve	Karthik, that specific Req is for the purposes of this session.
Leigh	@Dilipkumar - yes, the resumes for all students who participated on the chat will be saved and reviewed by recruiters
Steve	but, there's other req's on line that are for purposes of hiring.
Venkatswamy.Gandham	Am I sound in technical skills to qualify for a job at Intel
dilipkumar.gangapuri	<ul style="list-style-type: none"> • can I still do an internship after I graduate from school with my OPT.
Shardul.Navare	Hi, I am master's student in Electrical and Computer Engineering at Georgia Tech with backgrounds in Computer Architecture, VLSI, Digital System testing and Embedded Systems and interested in Pre and Post Silicon validation opportunities
Tom.Sommerville	@Scott - I like the sound of that, variation helps keep maximum interest :) What different types of projects have you worked on?
Leigh	@Dennis - we typically will look to hire students who have some relevant background for engineering positions

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Vinay.Vijendra Kumar Lakshmi	@Amanda: Thanks for all the help. I request your team to review my resume for Job Req no. 609815. Thanks!
Amanda	@Dilipkumar-I'm afraid our internships are open only to folks who are still completing a degree
Gokul.Krishnappa	Hi Steve, Scott, Leigh, Amanda
Steve	@Venkat, I don't see a problem. (the only problem is competition from others, which is why I recommend to review your resume)
Leigh	@Vinay - yes, we'll do that, thanks.
Amanda	Hello Gokul
Gokul.Krishnappa	I wanted to know about your team and location
dilipkumar.gangapuri	<ul style="list-style-type: none"> • What are the programming languages intel using for pre and post silicon verification and validation.
Venkatswamy.Gandham	thank you
Steve	I Gokul.
Gokul.Krishnappa	and do you currently have any openings in your team?
Dennis.Chapman	Leigh, I have some engineering experience but not software, are there entry level manufacturing jobs?
Leigh	@Gokul - we have large locations in Chandler, AZ, Portland, OR, Folsom, CA and Santa Clara, CA
Shardul.Navare	Hi, I am master's student in Electrical and Computer Engineering at Georgia Tech with backgrounds in Computer Architecture, VLSI, Digital System testing and Embedded Systems and interested in Pre and Post Silicon validation opportunities
Dharmesh.Chheda	Do we currently have openings in Folsom?
Vishal.Guntur	@steve.. i did a project on the design of a control ASIC for a VRM.. had to understand some of the specifications for the project which I think was a very good learning experience
Thomas.Hummel	@Steve .. is the same booth as Erin's?

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Steve	Our RTL is verilog, but we have used vhdl in the past. We use C/C++, assembly and perl, python as well as a language called specman primarily. We don't require a specific language in most candidates, as long as there is experience in a similar one.
Gokul.Krishnappa	And Can I know the criteria for prescreening of resume?
dilipkumar.gangapuri	when would hiring for RCG would start
Amanda	@Gokul-We currently have active job openings posted here: http://www.intel.com/jobs/usa/students/
Leigh	@Dennis - the chat today is primarily with regards to validation positions, not manufacturing positions. For validation positions we hire students with backgrounds in CE, EE and CS
Scott	Tom - the corei3,5,7 products are all programs that we worked on validated over the last few years.
Leigh	@Thomas - this is not the same booth as Erin's
Amanda	@Dilipkumar-We're currently hiring for RCG's and will continue through the Spring
Abhilasha.Jain	Thank you all for answering my questions
Thomas.Hummel	@Leigh .. ty .. is your team interested in undergrads who will be entry level by December?
Shardul.Navare	=CONCATENATE("@Leigh: Hi, I am master's student in Electrical and Computer Engineering at Georgia Tech with backgrounds in Computer Architecture, VLSI, Digital System testing and Embedded Systems and interested in Pre and Post Silicon validation opportunities. Will like to know further on this")
Tom.Sommerville	@Scott - Did you cover whole-chip Validation, or specific portions?

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Ganapathy.Kumar	=CONCATENATE("@ Steve - Apart from backgrounds in specific silicon architecture related courses, programming and validation protocols, are there any hardware specific skills that are expected from graduate student applicants who have not had opportunities as interns or co-op engineers?")
Leigh	@Shardul - I'm not sure what you're asking
Scott	Tom - yes whole chip validation
Steve	@Gokul, there's no set criteria, many have the same qualifications from their Classes,, Research Projects and Related work experience helps to distinguish you in a competitive market.
Shardul.Navare	@Leigh: what locations are we hiring for?
Vibudh.Mishra	@Scott...Thanks...What are the specific technical skills you are looking for other than C, C++ and Python.
Leigh	=CONCATENATE("@Thomas - Intel is currently assessing our hiring plans for the remainder of the year. This effort is to ensure we carefully plan the remaining hires for the year. We are on track to have the best year in our company's history and as such, we want to ensure we stay on that track. you will see some positions open for the rest of 2011 and more opening for positions in 2012")
dilipkumar.gangapuri	are there any positions for physical/component /cpu engineers or just validation engineers
Gokul.Krishnappa	Thank you steve
Thomas.Hummel	@Leigh .. again, ty
Leigh	@Shardul - we are hiring for Chandler, AZ; Hillsboro, OR; Folsom, CA; Santa Clara, CA
Leigh	@ Thomas - you're welcome
Tom.Sommerville	@Scott - sounds like a lot of ground to cover. I'm sure it keeps your group pretty busy. What options are there in your group for personal and professional development?

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Gokul.Krishnappa	Has the hiring process started, as I heard there will be little hiring activity during months of Nov and Dec
Leigh	@Dilipkumar - today's chat is only about validation engineering. You can check what other positions are posted at www.intel.com
Ganapathy.Kumar	@ Leigh - Is there a weightage on PhD degree holders vs MS degree holders for said position?
Steve	please re-ask if your question was not answered. also there's a way to see the log of this session.
Shardul.Navare	@Leigh: I am graduating in December 2011. Am I eligible for these positions?
Devorah.Hayman	I live in Phoenix and third year ASU student, Computer Systems Engineering. I would love to learn more about validation and try it out in internship
Hrishikesh.Sankpal	=CONCATENATE("Hi Steve.. I am a Fall 2010 Graduate with a Major in Electrical Engineering and am seeking a Validation Engineer position with Intel. I had a question regarding the onsite interview process. How do you determine whether the candidate is appropriate for the position, especially if there are candidates which possess the almost the same skillset. What are the priorities in skills that hiring managers usually look for?")
Scott	Vibudh - more technical info is gleemed during interviews but a solid broad technical understanding is very helpful.
Leigh	@Ganapathy - in general we hire about 1/3 BS students, 1/3 MS, 1/3 PhDs. However in validation specifically it is more MS level hiring
Vishal.Guntur	=CONCATENATE("@steve..we can't ignore impact of analog issues rt.. i mean let's say that the DVID requirement is 0.6 V and some noise/issue makes it impossible for the VRM to supply regulated output within the specified time or the voltage overshoots above the specified max level.. it could lead to some serious damage right?")
dilipkumar.gangapuri	what skills do you look in RCG for validation engineers

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Vibudh.Mishra	@Scott, Thanks.
Leigh	=CONCATENATE("@Devorah - You may have heard recently that our CEO announced that we would hire twice the number of interns in 2012! We are really excited about this, and it creates unique opportunities for students set to graduate in late 2012 and beyond. Here is the press release with that announcement. http://bit.ly/oX68fL ")
Steve	@Hrishikesh, I can tell you what I do. which is ask some questions that determine to me if a person can logically work through a problem with vague criteria.
Abhishek S.Shetty	@stevve: which are the most important skills needed for Validation. I have experience in Verilog and SystemVerilog
Mahmudul.Chowdhury	=CONCATENATE("Hello Steve, I am currently finishing my Master's in Computer engineering and will be graduating in December 2012, I have taken courses in analog integrated circuits, digital VLSI design project written in C++, is this a good fit for a validation engineering position?")
Lohithnaga.Sama	Hi Leigh,
Ganapathy.Kumar	@Steve - How does the validation engineer job vary for a PhD holder?
Steve	@Hrishikesh, that point is important to me, in terms of how it relates to the positions I hire for. But every manager is different in their interviewing style.
Ganapathy.Kumar	@Leigh- Thank You very much!
Leigh	@Devorah - so there will be a good amount of internship opportunities. Please make sure you've applied to the position for this chat and for any other internships you find at www.intel.com
Leigh	@Lohithnaga - hi
Leigh	@Ganapathy - you're welcome
Zuhaib.Sheikh	@Zuhaib - Hi
Lohithnaga.Sama	@ Leigh : Can I know, Is there any particular platform for validation

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Abhishek S.Shetty	@Steve: what do you look in an ideal candidate?
Steve	@Vishal, yes, overshoots can cause damage to the part, Validation has specific teams and people to validation Analog issues. Validate the analog interfaces.
Leigh	@Lohithnaga - it varies. Mobile Desktop and Server
Priyanka.Rao	Hi may I know what would be the skills you would look for in a candidate ?
Vishal.Guntur	@steve.. i did a project on the design of a control ASIC for a VRM.. had to understand some of the specifications for the project which I think was a very good learning experience
dilipkumar.gangapuri	what skills do you expect inRCG for validation engineers
Lohithnaga.Sama	Can I know a bit more about what technologies you used for validation
Abhishek S.Shetty	@leigh:what do you look in an ideal candidate?
Leigh	@Priyanka - in general we're looking for good GPA and demonstrated knowledge of your area of study through academic projects, research or internships.
Steve	@Abhishek: IMO the most important skills is the ability to problem solve. The most important charastic is motivation to do well at your work. Without the 2nd part, the 1st part will only happen spordically..
Devorah.Hayman	@Leigh Count me in!
Leigh	@Abhishek - I'll add on to Steve's comments. An ideal candidate can also communicate well and work well in teams
Priyanka.Rao	May I know th typical technical skills I need to have as a propective candidate
Abhishek S.Shetty	Yeah sure. I have prior experience fro 2yrs in Coding but not in validation. does it help
Steve	=CONCATENATE("@Mahmudul: Yes, you will have the proper things on your resume for validation. If you are serious about Validation, include it as a career objective on your resume, this will make you stand out... Managers love to hire people that definitely Want to do the work.")

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Scott	HI Priyanka - there's no set criteria, many have the same qualifications from their Classes,, Research Projects and Related work experience helps to distinguish you in a competitive market.
Amanda	Abhishek-what is your degree in again?
Rutvi.Joshi	=CONCATENATE("@ Steve: Hello, i am Rutvi Joshi pursuing Masters in Electrical and Computer engg at Univ of Colorado Boulder. I donot relevant experience in the field of system debug. So for an entry level engg, what do you look for in the applicant and how do you assess them ?")
Ajinkya.Urankar	@Steve: I am a Master's graduate from Cornell University in Electrical and Computer Engg. majoring in Analog VLSI Design/Circuit design.. I would like to know if Intel is currently looking out for graduates majoring in circuit design ?
Jacob.Haynes	What is the hiring timeline for the Summer '12 internships? Is there a way to speed it up (I have interviews currently taking place and have offers with deadlines fairly soon)
Abhishek S.Shetty	@Amanda: Electrical Engineering Masters. But after my under grad I worked in Electronic Manufacturing and Software Development
Steve	SORry guys, I've missed a few questions, I'll try to hop along the best possbile, please re-ask if I missed your ?
Amanda	@Abhishek-Sounds like a great foundation in education and skills
Leigh	@Jacob - you will find some internship positions at www.intel.com/jobs at this point. A lot of the positions will open in 2012. Unfortunately there's no way for us to expedite that
Abhishek S.Shetty	@Amanda: Yes, presently I am specializing in Verification through my research.DOes it help?
Priyanka.Rao	Hi , would you be considering resumes during this session ? How does it work ? What would be the things that would catch your eye if I may ask.

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Amanda	=CONCATENATE("@Ajinkya-We currently have active job openings posted here: http://www.intel.com/jobs/usa/students/ Not everything is posted yet though, as we are still working through our hiring plans for 2012. Keep an eye out over the next weeks and months as we get more jobs posted and begin the interview process!")
Steve	Validation needs skills of programming, also, the ability to understand the Design, the ability to understand the big picture, and the ability to be a detective, taking the bits of Evidence and solving for the root cause.
Amanda	@Ajinkya-Degree requirements are listed in each req
Leigh	=CONCATENATE("Be sure you visit all of our 5 booths to hear from multiple hiring managers. You can click on the top button within each booth and read about a specific hiring manager at Intel and his/her journey within validation roles! (Hint: Click on 'Next Booth' to be routed to each one)")
Vishal.Guntur	@Steve.. what kind of a team size would I be working in if I were to join your team?
Amanda	@Abhishek-Internships and research in verification does indeed help
vinit.apte	can you please give idea of how many recent graduates are you going to hire for the said job posting
Abhishek S.Shetty	So, by when can we expect to receive responses on our resumes?
Steve	@Rutvi, not required to have Validation experience to be hired, BUT, nice to have had some work experience (Internship. even if it's not Validation)
Amanda	=CONCATENATE("@Abhishek-Response times to applications can vary. Some managers quickly review candidates and pick up the phone and call for a phone screen. Others can take more time to review their applicants. The best thing you can do is apply to all jobs that you feel interested in and at least 80% qualified for! We have many recruiters that review resumes and this is a good way to be seen by all.")

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

dilipkumar.gangapuri	@Intel Managers thanks, This session is very informative, thanks again
Priyanka.Bhardwaj	=CONCATENATE("@Steve and Amanda: I am a recent(May 2011) graduate with a Masters of Science in Electrical Engineering. But I don't have the validation or fabrication background. I have the wireless comm and RF Engg background. Do you stiiil see any chances of me getting considered at Intel? Thanks")
Leigh	=CONCATENATE("@Vinit - there is no set amount for the positions we're discussing today. We haven't finalized hiring numbers over the next year. However with regards to internships You may have heard recently that our CEO announced that we would hire twice the number of interns in 2012! We are really excited about this, and it creates unique opportunities for students set to graduate in late 2012 and beyond. Here is the press release with that announcement. http://bit.ly/oX68fL ")
Abhishek S.Shetty	@ Amanda: I have been applying for all suitable openings, but never was lucky to get a call. I thought something is wrong with my skills.
Leigh	@Dilipkumar - thanks and best of luck to you
Kalpita.Jha	Hey Steve: System debug..the name sounds exciting..do you guys like hack into hardware and find bugs or something?? what kind of skills are required??
Steve	@Vishal: My team is about 8,, the size of teams are kept to about 5-15,, (hovering around 10)
vinit.apte	thanks leigh
Amanda	Abhishek-I can't be sure of your specific case, but we're not done hiring
Dhivya.Jeganathan	Hi Amanda
Abhishek S.Shetty	Thanks a lot for your time Amanda
Dhivya.Jeganathan	Im surrenting intening at Intl, Folsom
Amanda	Thank you for joining!

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Dhivya.Jeganathan	currently*
Steve	@Priyanka: Wouldn't be a problem for consideration in my mind, work experience == Good.
Leigh	@Dhivya - great, hope you're enjoying the experience!
Dhivya.Jeganathan	I enquired about a fulltime offer
Amanda	@Kalpit-We do have teams that focus on security, but not this specific group
Priyanka.Rao	=CONCATENATE("Hi Amanda ,Steve . I have internship experience where I was responsible for the design and implementation but limited experience with respect to Validation. However, that does not limit my skills in any way at all since I am confident I am a good problem solver. But that sometimes is still not good enough to be hired so I do get into a field I am interested in but have limited experience . Appreciate your time and pateince !")
Dhivya.Jeganathan	but since I'm graduating in May 2012
Kalpit.Jha	@Amanda, so what does this team do??
Dhivya.Jeganathan	they say they cant gaurantee me an offer
Dhivya.Jeganathan	is this the case for all managers?
Steve	@Kalpit, we don't call it hacking,, it's more of like Educated Investigations.. :-)
Alberto.Puggelli	=CONCATENATE("@Steve, @Leigh: Hi, I am Alberto Puggelli, a PhD student at UC-Berkeley. I work on functional validation of power management strategies (voltage islands, DVFS, etc.). Would your team be interested in my work? Have you openings for summer internships in the field? Thank you!")
Kalpit.Jha	@steve: sounds interesting!! :)
Leigh	@Alberto - great to hear from you!

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Priyanka.Bhardwaj	@Steve: Thank you for replying. I don't have any company work experience only Masters work exp. Would that be enough? Tho before Masters, I have done Website auditing, finding loop holes, bugs, vulnerabilities and fixing them. Thanks
Alberto.Puggelli	@Leigh: thanks, I would be very interested in working at Intel!
Steve	@Kalpit: Hackers don't have the ability to know everything about the system they are working on.. Since we have access to all of the designers, we Can know Everything! Oh the power. (but, yet the volume of knowledge is quite large.)
Amanda	@Alberto-We do have teams focused on Power specifically. Watch for their positions to open in the next couple weeks
Leigh	=CONCATENATE("@Dhivya - in some cases managers will make offers for full time positions before the internship ends, but that is not always the case. I'd encourage you to keep in touch with your intern manager after the internship ends, and also to apply to full time positions in other groups too!")
Leigh	@Dhivya - we give hiring preferences to our former interns
Dhivya.Jeganathan	oh thats nice
Rutvi.Joshi	@Steve: Yes I am an Intern. But it may not very relevant work. Thank You for your response!!
Steve	@Alberto: Yes, powermanagement Validation work is part of our charter, it's a growing area.
Priyanka.Bhardwaj	@Steve: Also, what system do you debug? the chips intel makes? the hardware?
Alberto.Puggelli	@Amanda, thanks! do you mean to check on Intel Jobs? Do you have any specific indication? Most jobs are called Summer internship and it is difficult to figure out the topic... thanks!
Steve	@Priyanka, I think that expereince!, it's quite applicable.

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Dhivya.Jeganathan	Is the Validation Event the only place I should post my resume for all the validation positions?
Mahmudul.Chowdhury	How many validation groups are there? Are all the groups hiring and if so, are the applications different?
Amanda	@Alberto-yes check back on Intel Jobs and apply directly. I would think Power Management Intern might be a likely title to search for
Kalpita.Jha	@steve: you guys do have access to design documents, btw, how do people with a background in VLSI, Analog and RF fit in your team?
SHIVA REDDY.BUSIREDDY	=CONCATENATE("Hi, I am Shiva from OSU. I am looking for full-time position. I will be graduating in December. My specialization is Computer architecture with interest in design and verification. I have experience in C,C++, verilog, system verilog, Perl, assembly language. Currently I have taken basic computer architecture, advanced microarchitecture courses and ASIC design/verification courses at OSU. Do you think i am qualified for the job.")
Leigh	@Dhivya - we'll consider all the applicants from this event - however I'd also encourage you to apply to any additional validation questions posted at www.intel.com/jobs
Alberto.Puggelli	@Steve: that sounds very interesting, do you think we can discuss further offline? I have already submitted my resume for this event...
Steve	@Priyanka, the processor. We validation the new prototypes and when everything appears to be in working order, we open the gate for Mass Production and sales.
Alberto.Puggelli	@Amanda: thanks, I'll keep checking!
Priyanka.Bhardwaj	@Steve, @Amanda, @Leigh, @Scott: Can I join you guys on Linked-in?

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Ajinkya.Urankar	@Steve and Amanda: I have applied to quite a few positions.. the norm I guess is that for a lot of positions, the candidates are rejected.. how does one make through the intial filterng process in order to get noticed for probably a phone screen ?
Dhivya.Jeganathan	Ok
Leigh	@Alberto - we're not doing offline communications from this chat. But we've met at Berkeley and SRC before, so you can follow up with me on campus!
Priyanka.Rao	=CONCATENATE("Hi Amanda ,Steve . I have internship experience where I was repsonsible for the design and implementation but limited experience with respect to Validation. However, that does not limit my skills in any way at all since I am confident I am a good problem solver. But that sometimes is still not good enough to be hired so I do get into a field I am in interested in but have limited experience . Appreciate your time and pateince !")
Dhivya.Jeganathan	Will you'll begin the hiring process immediately?
Dhivya.Jeganathan	or will it go on throughout 2012
Priyanka.Bhardwaj	@Steve: I got it. Thanks. I have applied quite a number of times at Intel but never got a call, do you think anything else needs to be done?
Steve	@Kalpit, yes, we have access to everything. Analog is a Validation area.. VLSI design is only indirectly used while investigating failures (we don't write anything.)
Leigh	@Priyanka - please join our LinkedIn Student Group: . You can join by following this link: http://www.linkedin.com/groups/Intel-Student-Lounge-3686572?mostPopular=&gid=3686572
Vishal.Guntur	@steve... thanks for answering all my questions !.. have been reading through responses to others' queries.. i look forward to hearing back from your team!

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Alberto.Puggelli	@Leigh: sure, sorry, I haven't recognized you... still looking for a position, unfortunately I haven't been able to discuss about my project with anyone yet...
Bart.Gasiewski	Hello, what are they responsibilities of a new employee for your division?
Devashish.Bhatia	@ Steve : Hello
Steve	@Shiva, yes, qualified.. please ensure your resume is quite clear on your capbabilities and career intentions.
Devashish.Bhatia	How are you doing
Kalpita.Jha	@steve: I find your work interesting!! please have a look at my resume! It was great talking to you! Thanks for your time and thanks for the valuable information!
Amanda	@Zuhaib-generally if a phone screen goes well, then you will be contacted for an interview and receive a decision from there
nazia.kouser	@steve Hello Steve,I need an advice as to what jobs i should be applying at intel.I do have a gap after graduation for reasons but have the right skills to enter VLSI industry.Please advice.
Scott	Bart - varies widely depends on the division and the specific role.
Justin.Kurian	Where would I fit in as a chemical engineer at Intel?
Devashish.Bhatia	@ Steve: I graduated in May 2011 with a MS in EE
Amanda	@Zuhaib-How much time that takes varies
Priyanka.Bhardwaj	@all the Intel Managers: Thanks for helping. This chat session was really helpful for all of us. Thanks! :)
Devashish.Bhatia	GPA: 3.5....VLSI (analog/digital)

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Utsav.Lokinindi	=CONCATENATE("@Steve :I am Utsav Lokinindi , a graduate student from Texas Tech University . I am graduating this december 2011 and I have been actively looking for job positions with Intel . I have my course work focussed in Digital Design , Testing and Computer Architecture with strong trouble shooting and validation skills . I am current working as a student manager at the University through which I have mastered the managerial skill set . I have a sound knowledge with Verilo")
Alberto.Puggelli	@All, thank you for your help. Best regards!
Leigh	@Justin - most Chemical engineers work in our Technology Manufacturing Group doing process engineering roles. Or depending on your experience, you might also be a fit in the Corporate Services group
Steve	@Priyanka, Good looking resume with good looking expereince, that's my summary of what will work.
Justin.Kurian	@Leigh, thanks!
Amanda	@Zuhaib-we tend to focus on 3.0 plus
Martin Raith	=CONCATENATE("Do you have any chance to be considered (when applying for a job in validation) as a theoretical physicist (currently being Ph.D. student with masters degree), working on spintronics, silicon qubit devices, and quantum computation, compared to all the specialized computer engineering guys?")
Leigh	@Priyanka - glad the session has been useful
Steve	@Kalpit, no problem.
Amanda	@Zuhaib-In rare cases, I imagine it could
Mahmudul.Chowdhury	@Steve, @Amanda, once I have applied, is it possible to go back and change/update my resume?
Amanda	We definitely try to avoid that
nazia.kouser	@Amanda - Hello Amanda,I need an advice as to what jobs i should be applying at intel.I do have a gap after graduation for reasons but have the right skills to enter VLSI industry.Please advice.

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Steve	@Nazia, explain the gap in your resume, that will help managers reading it. (Gaps always make me wonder, knowing why the Gap (ANY reason !) would help.
Devashish.Bhatia	@Steve: Took courses on CMOS,ASIC,ARCHITECTURE,VERILOG,ANALOG DESIGN
Devashish.Bhatia	FPGA
Leigh	@Mahmudal - you can update your resume in your online profile at any time. It is a good idea to alwasy keep it up to date
Amanda	@Nazia-As long as you're within 18 months of graduation, you're still considered an RCG
Steve	@Utsav, everyting sounds good there..
nazia.kouser	@Steve Yes,I have a valid reason. I took a break to care of my kid.Otherwise I would never have been with no work.Thanks
Amanda	@Zuhaib-I believe so
Devashish.Bhatia	I completed my Masters in Electrical Engineering from San Jose State and graduated in May 2011. I am presently looking for Full-Time opportunities
Scott	Hi Martin - depends on the specific job and your skill set/interests.
Fred.Love	@Steve has the Ivy Bridge paradgim shift effect your test processes?
Joseph.Katz	=CONCATENATE("Hi. I am currently a senior at Purdue University, looking for a summer 2012 internship before I begin my PhD work. I met some Intel reps at SRC Techcon this year (hi Leigh!) and am very interested in working for Intel. I'm interested in several positions I see posted online, so I'm wondering if I should apply for several or just one at a time.")
Steve	@Devashish, that resume would be more geared toward design, if you would prefer to do validation, you would need to make your Career objective state that.
Samik.Biswas	@ Steve: Hi, I am completing my graduation this december and looking for a full time position, I have applied on the intel site and still looking forward for a response

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Amanda	@Nazia-Focus on regular experienced positions and highly leverage any internship work you did while still in school
atul.balani	@amanda: i have been working on cascade prob station for chip verification...is that of any help
Leigh	@Joseph - hi! good to hear from you. Definitely apply to all of the positions you are interested in
Amanda	@Atul-I'm not sure what your question is
Keyur.Shah	Hello steve I am Keyur I recently graduated from University of Texas at Arlington
nazia.kouser	@Amanda.Thanks
shayan.sengupta	=CONCATENATE("hey hi i am shayan...i am doing masters in EE...i have experinece in vhdl/verilog programming...i also know scripting languages like c/c++, tcl, perl...i am currently doing my masters thesis on reconfigurable computer architecture....i am also well veresed with basics of VLSI design, Timing issues and P&R...so does my skill set match the validation position or am i lagging on some???"")
Keyur.Shah	I have done MS-Electrical Engineering and my majors is signal processing and video processing
atul.balani	@amanda:casacade prob stations...for chip verification
Fred.Love	@Amanda Do you review resumes and give suggestions toward tailoring the current draft for a particular position?
Keyur.Shah	are there some open position in field of signal processing and video processing for which I can be considered
Leigh	@Fred - we are not able to review individual resumes today. Your career services office is a great place to help with that
Scott	Shayan - no your skills look like you could be a good match in validation.
Amanda	@Atul-if you're asking whether previous experience helps, then yes. Internships, research, work experience are all valuable
Joseph.Katz	@Leigh- Good to hear from you too! I'll do that. When should I expect to hear back from different possibilities?
Fred.Love	Thank you Leigh!

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Leigh	=CONCATENATE("@Joseph - Response times to applications can vary. Some managers quickly review candidates and pick up the phone and call for a phone screen. Others can take more time to review their applicants. The best thing you can do is apply to all jobs that you feel interested in and at least 80% qualified for! We have many recruiters that review resumes and this is a good way to be seen by all.")
smitha	Hi Amanda.. Will it help if we have previous experience in software testing as part of validation and verification
Steve	@Fred.. ummm,, I might not know as much about that as yourself.. Educational for the board?
shayan.sengupta	@scott i have for job no 609815 do i need to apply to some other jobs too....i hope you would review my resume after the event...
Steve	@Keyur.. hello.
Keyur.Shah	@steve Hi
Leigh	@Shayan - Yes, please also apply to any positions that interest you and for which your skills are a good fit to improve your chances for consideration
shayan.sengupta	thanks leigh i will surely do that
Steve	@Keyur, we do have Graphics Chips and Validation groups for those chips yes.
Scott	Hi Smitha - yes previous experience in software testing is always valued in certain groups within Validation.
Fred.Love	@Steve Thank you for answering. I guess a better question to ask is if your test processes are different for different product lines? Does the i5 undergo a different battery of test than the Pentium
nazia.kouser	@Steve- Can you reply to my response please.
smitha	Thanks Scott.
Akshay.Deollikar	what is the most challenging part in job?which languages are used ?
Akshay.Deollikar	IP develoment done by your group ?
Amanda	@Nazia-please repost the response

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Samik.Biswas	@ Leigh: Hi it was great meeting your team at the USC career fair.
Joseph.Katz	Thanks Leigh! I hope to hear from you again.
Keyur.Shah	@Steve thanks so will my resume be reviewed after the event, I have done some good projects in the domain of Signal, Image and Video Processing using C/C++ and matlab
Leigh	@Samik - yes, that was a busy day!
vinit.apte	just curious to know are you going to give more interview calls to suitable candidates for the position after this event
nazia.kouser	@Steve.. As a reminder, you said if I can give you the reason for the gap after graduation and I did give saying I needed to take care of my kid.
Amanda	@Nazia-It is best to address this in a phone screen or consider listing maternity leave as the reason for the gap in your cover letter.
Steve	@Zuhaib, my group doesn't have specific positional differences for Education Level. (that is in regards to responsibilities.) More responsibility is given to those that have proven their capabilities with tasks that they have owned.
Samik.Biswas	@ Leigh: I had submitted my resume there and am looking for a fulltime position as am graduating this december
Scott	Akshay - most challenging for me is keeping up with all the latest technology we use - for languages we use C, C++ and Python mostly. IP development is not done in our group.
Samik.Biswas	I have applied to various positions at the intel website too
Steve	@Fred, we have mostly converged Validation processes, so all of our processors undergo about the same type of Validation. (some differences)
Leigh	@Samik - good to hear that you've applied directly to positions at www.intel.com/jobs . You are following the right steps to be considered for jobs. You will see more positions being posted as we get later into this year and early next year
nazia.kouser	@Amanda-- Thanks.I will do so.

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

atul.balani	@amanda: i have understanding of comp architure ...does it help
SHIVA REDDY.BUSIREDDY	=CONCATENATE("Hi ,I am Shiva from OSU. I am looking for full-time position. I will be graduating in December. My specialization is Computer architecture with interest in design and verification. I have experience in C,C++, verilog, system verilog, Perl, assembly language. Currently I have taken basic computer architecture, advanced microarchitecture courses and ASIC design/verification courses at OSU. Do you think i am qualified for the job. If i do , can you please look into my profile.")
Steve	@Keyur, yes it will be reviewed.
Fred.Love	@Scott Is C used for hardware interface or more for automation of test processes?
Scott	Atul - yes knowledge of Computer architecture is helpful.
Leigh	@Shiva - we have all resumes for students who posted them for this virtual event and they will be reveiwd by recruiters.
Keyur.Shah	@steve thank you so much. Is there any other way by which I can improve my chances to get an interview call from Intel. Many of my friends work at intel and they always kepp telling me how exciting it is
Scott	Fred - more for automation
Leigh	@Keyur - make sure your friends at Intel are putting you in the system as an employee referral
nazia.kouser	@Scott- Hello Scott,I need an advice as to what jobs i should be applying at intel.I do have a gap after graduation for reasons but have the right skills to enter VLSI industry.Please advice.
vinit.apte	@leigh what tools are you using in your tram for validation purposes
atul.balani	@scott:i dont have experince of working in companies but i have experince in working with actual chip verification using prob station in my university...does it help

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Amanda	=CONCATENATE("@Nazia-If you aren't sure about where you may fit at Intel, or want to see more information on the types of roles and people we have within Intel, feel free to go to the student center and access an application we have published called 'Where do I fit?'. Here is a direct link to that application! http://www.intel.com/jobs/students/WDIF/WDIF.htm ")
Keyur.Shah	@Leigh thanks a lot but most of them have joined recently in last 2-3 months. will their referral still have weightage
Fred.Love	@Steve Thanks again... That makes a lot of sense. Would you mind telling me a little bit how are the performance requirements set? Is that customer driven or do the architects provide specs that they require be met?
Amanda	@Keyur-Of course!
nazia.kouser	@Amanda sure,I Will check
ganesh.viswanathan	Hi Steve, i am Ganesh, i am looking for intern position in summer 2012,i am from Computer science background,I have done interesting projects in my course work.
Leigh	@Vinit - i am a college recruiter, so I do not currently work on a validation team. Your question should be directed to Scott or Steve who are both validation managers
Sudeepti.Balepur	Hello @ All managers: I'm a CS major, graduating in Dec 2011 with a MS. I am on the lookout for a fulltime job as I graduate... Can you please tell me the requirements from a CS Stadnpoint and How I could fit in??
Steve	@Zuhaib.. I don't know of any managers that take university Ranking into consideration..
Keyur.Shah	@amanda thanks a lot I wil definitely tell them to put me on the referral list
Bryan.Winther	Are there any hardware design/debug opportunities available in Colorado?
vinit.apte	ok thanks
Scott	Vinit - can you repeat your qusion?
ganesh.viswanathan	Let me know the requirements for an intern position

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

vinit.apte	@Scott or Steve: what tools are you using in your team for validation
Leigh	@Bryan - we do have a design center in Fort Collins, CO. you can find more information on that at www.intel.com
Steve	@Zuhaib: Well known Universities tend to peak the interest of managers, but, in reality the decision is about the person and their resume more than which University.
PRATIK.PATEL	@Steve:Hiii I am Pratik here, I did my Masters in Electrical engineering at California State University, Northridge (DEC, 2010), and looking for Regarding recent college graduate (RCG) job position in the field of VLSI.
Amanda	@Bryan-also look at our Longmont, CO site
Scott	Vinit - we have a number of internally developed tools that are used for our validation testing.
NIKHIL.AWACHAT	@Leigh : Hiii, as an intern which all languages or software should be known
PRATIK.PATEL	=CONCATENATE("@steve:My coursework and academic projects emphasizing on Computer Architecture (5-stage DLX pipeline, cache polices etc), VLSI Design, FPGA/ASIC Design Methodology and Optimization, Verilog HDL: Modeling, Simulation and Synthesis, System on Chip, Digital Systems Structure, Diagnosis and Reliable Design of Digital Systems and Digital System Design with Programmable Logic. I have knowledge of programming languages such as C/C++ and scripting languages such as PERL. I have knowledge Make utility")
PRATIK.PATEL	@steve:. I have knowledge of programming languages such as C/C++ and scripting languages such as PERL. I have knowledge Make utility also.
Leigh	@Pratik - you are still considered a recent college graduate as long as you within 18 months from your graduation date.

Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE

Steve	@Vinit: Our RTL is verilog, but we have used vhdl in the past. We use C/C++, Assembly and perl/python as well as a language called specman. We don't require a specific language in most candidates, as long as there is experience in a similar one.
vinit.apte	so since you use internal tools do you train new grads on how to use them
Sudeepti.Balepur	Hello Steve,Scott: I'm a CS major, graduating in Dec 2011 with a MS. I am on the lookout for a fulltime job as I graduate... Can you please tell me the requirements from a CS Stadnpoint and How I could fit in??
Fred.Love	@Scott Does you team work with many industrial standard test benches such as SPEC or do you use all properitary standards?
nazia.kouser	@Scott Hello Scott,I need an advice as to what jobs i should be applying at intel.I do have a gap after graduation for reasons but have the right skills to enter VLSI industry.Please advice.
Leigh	Please ask all questions in this public chat forum. We are unable to manage/accommodate private chats due to the volume of attendees. As a side note, the chat transcripts will be posted after this event wraps up, so don't worry if you miss something!
PRATIK.PATEL	@Leigh: Thnak you.
ganesh.viswanathan	@Leigh Tnxs
Sudeepti.Balepur	Hello @ All managers: I'm a CS major, graduating in Dec 2011 with a MS. I am on the lookout for a fulltime job as I graduate... Can you please tell me the requirements from a CS Stadnpoint and How I could fit in??
PRATIK.PATEL	@Liegh: Do you have any postion where i mybe good FIT.

**Intel Virtual Chat
Chat Transcript 19/OCT/2011
Meet Steve - System Debug Manager, PVE**

Amanda	=CONCATENATE("@Sudeepti- If you aren't sure about where you may fit at Intel, or want to see more information on the types of roles and people we have within Intel, feel free to go to the student center and access an application we have published called 'Where do I fit?'. Here is a direct link to that application! http://www.intel.com/jobs/students/WDIF/WDIF.htm ")
Bryan.Winther	=CONCATENATE("@Leigh: Appreciate the info. Little different type of question. I graduated in 2009 with a EE degree from the University of Notre Dame and since completed a two year service program (similar to TeachForAmerica) in which I also received a Masters of Education (May 2011). While the M.Ed. does not directly apply to engineering positions, would I still qualify as a recent graduate?")
PRATIK.PATEL	@Leigh: If possible, van ou please refer to my resume
Steve	@Pratik; your qualifications seem fine.
Sudeepti.Balepur	=CONCATENATE("Hello @ All managers: I'm a CS major, graduating in Dec 2011 with a MS. I am on the lookout for a fulltime job as I graduate... Can you please tell me the requirements from a CS Stadnpoint. I have programmign experince in various languages along with Internship experiences... Since I am a CS Major, with not much knowledge of VHDL.. will I still be considered for jobs @ Validation")
ganesh.viswanathan	@steve,Can i get a response from you
Anton.Sidorov	I have PhD in ECE. I am working with graphene. I have 7 publications about graphene and nanotechnology and 2 US Patents. Do you have any job for PhDs engineer?
PRATIK.PATEL	@steve; if possible, can youplease review my resume, i have done also done good academic projects adn also proraming project
nwokoma1971.Ajuzie	Can Doctoral degree holders be considered for System debug

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Meet Steve - System Debug Manager, PVE**

Steve	@Sudeepti: We work with C/C++/Csharp, Assembly, Pearl, Python some other scripting languages.
vinod.reddy	Hi @Amanda I am RCG student specialization in Digital & Mixed signal design with experience as a Unix admin and FPGA Design engineer.I am good at c, perl,C++,verilog & Computer architecture. Does my skillset match for System Debug engineer.
Amanda	@Anton and Ajuzie-we do hire Phd's
Fred.Love	@Steve Thanks again... That makes a lot of sense. Would you mind telling me a little bit how are the performance requirements set? Is that customer driven or do the architects provide specs that they require be met?
Leigh	=CONCATENATE("@Bryan - that's a technicality I'm not sure about. We'd obviously be interested in you for the EE degree, for which you'd fall out of the RCG timeline. However since you have a MS since then, you may still qualify. I'd encourage you to still apply for RCG positions and you can explain teh situation in more detail if you are contacted by a recruiter")
Steve	@Ganesh about Internships?
ganesh.viswanathan	ya
Anton.Sidorov	I have applied for many many positions but no luck. Is it just me?
Sudeepti.Balepur	@Steve:Thnaks for your Response.. I have program mainly in C/C++ and intern experience in C#... I have done web design and Developement as well... Can you please tell me more about your Team and what are you looking for from RCGs??
pratik.dahale	Hello @managers, I Pratik Dahale and will be graduating in Dec 2011. I have applied to lots of openings in Intel that matched my skill set. Can you please review my resume. Also, when will be Intel starting its hiring again ?

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Meet Steve - System Debug Manager, PVE**

Leigh	@Anton - I can speak specifically to your situation as I'm not reviewing your resume. We do have hundreds of applicants for every position so they are very competitive.
Sudeepti.Balepur	@Steve:Thnaks for your Response.. I program mainly in C/C++ and intern experience programming in C#... I have done web design and Development as well... Can you please tell me more about your Team and what are you looking for from RCGs??
Leigh	@Anton - I *can't*
Scott	Hi Vinod - yes your skill set could be a match for what we would look for in a Debug Engineer.
Steve	@Ganesh, we do have interenships and had many in the group last Summer, it's a program we like to keep doing, the tasks are vaired, best thing for the Interns is the experience for the resume, best thing for the manager is a peak at the capabilities.
Amanda	Pratik-We currently have openings for RCG positiosn and more will be coming. intel.com/jobs
pratik.dahale	@Amanda, I have been applying to every postion till date.
Amanda	Excellent Pratik!
ganesh.viswanathan	@Steve So non experienced candidates have no chance of getting into intern positions
Steve	=CONCATENATE("@Sudeepti: An RCG starts out be learning one of the Validation Tools, possibliy writing tests or doing some test planning, they later graduate to do Debug on the Systems at frist to solve Software issues and then later with more experience work on HW issues.")
Steve	@ganesh: No,.. not the case at all. Internships are to Provide that experience.. :-)
ganesh.viswanathan	ya
Steve	slowing to a crawl.

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Meet Steve - System Debug Manager, PVE**

Sudeepti.Balepur	@Steve:Great to hear this.. This sounds a lot like where I would fit in..!! I have applied online to the req.. I have prior Intern Experience with Intel as well (RR Site)... I would like to get in touch and discus more about the same..
Steve	if your question wasn't answered, please Re-ask.. (you can also view the log of this session)
Fred.Love	@Steve Thanks again... That makes a lot of sense. Would you mind telling me a little bit how are the performance requirements set? Is that customer driven or do the architects provide specs that they require be met?
Amanda	Please ask all questions in this public chat forum. We are unable to manage/accommodate private chats due to the volume of attendees. As a side note, the chat transcripts will be posted after this event wraps up, so don't worry if you miss something!
Shreyas.Krishnappa	Which language is mostly preferred : Verilog or System Verilog ...
ganesh.viswanathan	what area of specialisation in computer science you hire the interns
Steve	@Fred: Industry Standard Benchmarks drive our performance goals..
Fred.Love	@Steve Thank you!
vinod.reddy	@Scott Can you please tell me location of your team and what type of a candidate you are looking for.
PRATIK.PATEL	@Steve: If possible can you please, review my resume? I have done good academic projects and prograaming and scripting that I have described in my resume
ganesh.viswanathan	@Steve what area of specialisation in computer science background u hire the interns
vinod.reddy	@scott I mean candidate must have any previous experience
Aparna.Sundararajan	@Steve: Hi Steve.. I am Aparna. I just joined this chat room. What is the major work that you do at System level debugging ?
Amanda	@Shreyas-Both Verilog and System Verilog are used at Intel

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Meet Steve - System Debug Manager, PVE**

Steve	@Ganesh: I don't have a great answer for that, since my team specifically doesn't do a lot of Programming. but as stated earlier that the group as a whole uses C/C++/Assembly and Various scripting languages.
Steve	once you know 2 scripting languages, you can figure out the rest.
ganesh.viswanathan	Thanks Steve
Sudeepti.Balepur	@Scott,Steve: If possible can you please, review my resume? I have academic and Intern experience with various prograaming and scripting lang. that I have described in my resume
Scott	Vinod - my teams are located in Oregon and California - our needs are varied so depends on what your key interests/skills are
Leigh	Hi Sudeepti - we're not able to reveiw individual resumes today
Leigh	@Sudeepti - but all resumes from people who applied to the position associated with this chat are availalbe for our recruiters to review after the event
Aparna.Sundararajan	@Leigh: What are the open positons in your team and what will be the job responsibility ?
Leigh	@Aparna - I work in college recruiting, not on a validation team
ganesh.viswanathan	@steve what positons are open for interns
Sudeepti.Balepur	ok.. thanks for the infromation.. Helps a lot in knowing that you will be reviewing them... Appreciate organising the event and informing about the Diffrent optiosn adn groups @ Intel..!
Sudeepti.Balepur	have a godo day..
Steve	@Ganesh, I have one position on my team of 8,, (as does about every other manager when it comes to summer interns.)
Sudeepti.Balepur	*have a good day..!
Amanda	Internships can be reviewed at intel.com/jobs , search by internship

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Meet Steve - System Debug Manager, PVE**

Steve	Thanks Sudeepti, you too.
Aparna.Sundararajan	@Steve: what will be the job responsibility ?
ganesh.viswanathan	@steve so what positon u have
Mahmudul.Chowdhury	Thank you, Steve, thanks for all the information...
Steve	@Aparna.. the internships generally have tasks assigned in the begining and can be quite varied. THE best tasks for internships are those can be be complted Fully during the duration.
Steve	so usually they are Smallish programming tasks or assiting other engineers with Bigger tasks..
Steve	working on a few HW Issues, etc.
Steve	(but I speak for my group here)
Aparna.Sundararajan	@Steve: On what do you work basically ?
kenan.modi	Hi, I have expertise in Embedded system, do you consider embedded engineers for this position?
Amanda	@Kenan-I would think there would be embedded roles at Intel that you would enjoy more
Steve	@Aparna: THE group as a whole (wider than my individual group) Validations new Processor Prototypes, HW issues are resolved and then fixed and then a new prototype is built, when we get all of the bugs out, it may be mass produced and sold.
Amanda	@Kenan-may want to search for roles with Embedded or SOC
Steve	@Aparna, we look for, find and resolve HW BUgs that would prevent the sale of the procesor.
kenan.modi	@Amanda- thanks
Steve	@Zuhaib: Power Management!
Aparna.Sundararajan	@Mike: So do you prefer candidates with instrumentation experience too.. like working on oscilloscopes, analyzers, etc .. ??

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vinod.reddy	Thanks for reply @Scott If possilble please review my resume. My Experience and Skillsets described briefly in resume.
Steve	@Zhuaib: the power consumption of the processors (or phones) is an area ripe for more work and enhancements.
Amanda	@Vinod-We won't be able to review all resumes today, but all applicants are considered
Pinto.Akkara	@Steeve - I am a masters student in Comp.Engg - Specialized in VLSI/Embedded Systems. I have got professional experience of 3 years in embedded software development. Could you please tell me what is the skill set you are looking for. Thanks a lot.
Steve	The closest job to embedded is in a area called BIOS, the code that initializes the processor when it's turned on.
Steve	This isn't a big area for us, but one that must be staffed, since we create our own Specialized BIOS for Validation.
Pinto.Akkara	Ok.I am open to validation domain jobs too. I wanted to know, if my experience in C/C++/Perl will help in this domain.
Pinto.Akkara	Thanks a lot for your time.
Steve	we do other work with C/C++/Perl for our work... (Test Enviornments for example) so those skills still apply (just not for the embedded purpose)
Pinto.Akkara	Thank you.
Steve	any insite you want us to know from the student perspective..
Steve	insight
Aparna.Sundararajan	@Steve: I am interning with AMD currently and I work on post silicon validation.. I have got pretty good debugging skills... So what are my chances to get into this position ?
Steve	@Aparna, pretty good with that experience.
Amanda	Your welcome!
Steve	@Aparna: How does it look to get into AMD full time,, (may I ask.)
Amanda	Any other questions folks?
Steve	Thanks Amanda and Leigh for your support.

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Meet Steve - System Debug Manager, PVE**

Aparna.Sundararajan	@Steve: I am exploring all available options open to me
Leigh	and thanks Scott and Steve for all the insight on validation at Intel!
Steve	@Aparna, very wise.
kiranjeet.kaur	hi there, will you be accepting resumes for all your locations
PRATIK.PATEL	@scott:Hiii I am Pratik here, I did my Masters in Electrical engineering at California State University, Northridge (DEC, 2010), and looking for recent college graduate (RCG) job position at Intel.
Amanda	Kiranjeet-yes
Leigh	@Kiranjeet - yes, we are hiring for all Intel locations
PRATIK.PATEL	=CONCATENATE("@scott:My coursework and academic projects emphasizing on Computer Architecture (5-stage DLX pipeline, cache polices etc), VLSI Design, FPGA/ASIC Design Methodology and Optimization, Verilog HDL: Modeling, Simulation and Synthesis, System on Chip, Digital Systems Structure, Diagnosis and Reliable Design of Digital Systems and Digital System Design with Programmable Logic. I have knowledge of programming languages such as C/C++ and scripting languages such as PERL.")
Aparna.Sundararajan	@steve: do you suggest any courses to be taken in the school ?
PRATIK.PATEL	@Scott:. I have knowledge of programming languages such as C/C++ and scripting languages such as PERL
Leigh	@Pratik - I think we've addressed your questions earlier in the chat
Steve	@Aparna, Computer Arch. but your experiece will overshadow that..
PRATIK.PATEL	@ Liegh : oh all right
Dara..	Amanda, Do you have 12-month internships ?
vinit.apte	thanks intel it was a nice event and really informative
Leigh	@Dara - yes, we have internships that range from 3 months to 12 months
Amanda	Thanks for joining Vinit!

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Meet Steve - System Debug Manager, PVE

Srilakshmi.Nallapati	@Steve: I am looking for RCG position at Intel. I have good knowledge in VLSI design, computer architecture and testing methodologies. Do you have any positions that fit my profile?
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